



Journal of Emerging Technologies and Innovative Research

An International Open Access Journal Peer-reviewed, Refereed Journal

www.jetir.org | editor@jetir.org An International Scholarly Indexed Journal

Certificate of Publication

The Board of

Journal of Emerging Technologies and Innovative Research (ISSN : 2349-5162)

Is hereby awarding this certificate to

Neeraj Gupta

In recognition of the publication of the paper entitled

**DESIGN CHARGE-PUMP PHASE-LOCKED LOOP (CPPLL) USING 0.35 μ m
VLSI TECHNOLOGY**

Published In JETIR (www.jetir.org) ISSN UGC Approved (Journal No: 63975) & 7.95 Impact Factor

Published in Volume 5 Issue 5 , May-2018 | Date of Publication: 2018-05-30

Parisa P

EDITOR

EDITOR IN CHIEF

JETIR1805556

Research Paper Weblink <http://www.jetir.org/view?paper=JETIR1805556>

Registration ID : 182478

