



# Journal of Emerging Technologies and Innovative Research

An International Open Access Journal Peer-reviewed, Refereed Journal

[www.jetir.org](http://www.jetir.org) | [editor@jetir.org](mailto:editor@jetir.org) **An International Scholarly Indexed Journal**

## Certificate of Publication

The Board of

Journal of Emerging Technologies and Innovative Research (ISSN : 2349-5162)

Is hereby awarding this certificate to

**Rajkumar Sarma**

In recognition of the publication of the paper entitled

**Significance of Design Constraints in Low-power High-speed VLSI Circuit**

Published In JETIR ( [www.jetir.org](http://www.jetir.org) ) ISSN UGC Approved (Journal No: 63975) & 7.95 Impact Factor

Published in Volume 6 Issue 2 , February-2019 | Date of Publication: 2019-02-12

*Parisa P*

EDITOR

*[Signature]*

EDITOR IN CHIEF

**JETIREL06013**

Research Paper Weblink <http://www.jetir.org/view?paper=JETIREL06013>

Registration ID : 305961





# Journal of Emerging Technologies and Innovative Research

An International Open Access Journal Peer-reviewed, Refereed Journal

[www.jetir.org](http://www.jetir.org) | [editor@jetir.org](mailto:editor@jetir.org) **An International Scholarly Indexed Journal**

## Certificate of Publication

The Board of

Journal of Emerging Technologies and Innovative Research (ISSN : 2349-5162)

Is hereby awarding this certificate to

**Cherry Bhargava**

In recognition of the publication of the paper entitled

**Significance of Design Constraints in Low-power High-speed VLSI Circuit**

Published In JETIR ( [www.jetir.org](http://www.jetir.org) ) ISSN UGC Approved (Journal No: 63975) & 7.95 Impact Factor

Published in Volume 6 Issue 2 , February-2019 | Date of Publication: 2019-02-12

*Parisa P*

EDITOR

EDITOR IN CHIEF

**JETIREL06013**

Research Paper Weblink <http://www.jetir.org/view?paper=JETIREL06013>

Registration ID : 305961





# Journal of Emerging Technologies and Innovative Research

An International Open Access Journal Peer-reviewed, Refereed Journal

[www.jetir.org](http://www.jetir.org) | [editor@jetir.org](mailto:editor@jetir.org) **An International Scholarly Indexed Journal**

## Certificate of Publication

The Board of

Journal of Emerging Technologies and Innovative Research (ISSN : 2349-5162)

Is hereby awarding this certificate to

**Shruti Jain**

In recognition of the publication of the paper entitled

**Significance of Design Constraints in Low-power High-speed VLSI Circuit**

Published In JETIR ( [www.jetir.org](http://www.jetir.org) ) ISSN UGC Approved (Journal No: 63975) & 7.95 Impact Factor

Published in Volume 6 Issue 2 , February-2019 | Date of Publication: 2019-02-12

*Paria P*

EDITOR

EDITOR IN CHIEF

**JETIREL06013**

Research Paper Weblink <http://www.jetir.org/view?paper=JETIREL06013>

Registration ID : 305961

