

# Enhanced Power Efficiency in Piezoelectric Energy Harvesting System Using CMOS Technology

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## Abstract:

Energy harvesting is the emerging technology, such device convert ambient energy into electrical energy. Various types of ambient energy sources such as solar, thermoelectric, blood pressure, vibration microgenerator, piezoelectric push buttons and ambient radio frequency. Energy harvesting through piezoelectric is generating very small amount of power. This limits its use to low energy electronics such as wearable electronics, wireless sensor network and longer term low power sensor etc. Here piezoelectric transducer is source for energy harvesting. Piezoelectric is capable of generating electrical energy when a vibration is applied on them. The restricted limit of limited power source and the requirement for supply vitality for an existence time of a framework. There is a necessity for self power gadget. Vitality gathering focused from piezoelectric transducer. Utilizing piezoelectric transducer change over electrical vitality by utilizing rectifier. The output voltages of piezoelectric transducer are 1V only. This voltage is not enough to drive circuits. Hence boost converters also included in this circuit. To reduce the overall power dissipation the rectifier and boost circuit are implemented using CMOS technology.

## Keywords:

ACDC power conversion, energy harvesting, piezoelectric transducer, active rectifier, Boost converter.

## 1. INTRODUCTION

Energy harvesting has been largely researched and applied into quite a lot of purposes such as into wireless sensor nodes (WSNs), implantable, wearable microelectronic instruments and many others. Taking competencies of speedy technological developments in low-energy CMOS design. These functions permit for the usage of electronics into tough to reach locations reminiscent of inside the human body. However, these implanted or integrated electronics use batteries requiring periodic substitute. Therefore, these applications normally have very strict requirements for battery existence. To solve the problems related to powering of the electronics, power harvesting from an ambient power source corresponding to electromagnetic signal, wind, mechanical vibration, RF, and thermal vibration are considered to be one of the most promising solutions. Amongst these ambient energy sources, piezoelectric vibration based vibration harvesting procedure has obtained an excellent deal of concentration on the ground that it fits ease of integration and a reasonable power density.

Technological advancements in low-power CMOS techniques have pushed fast progress of sensor electronics corresponding to implantable and wearable sensors methods. The powering of such sensor techniques has remained a most important trouble considering that the ordinary batteries can leak leading to critical wellness issues in sufferers. In addition, in natural sensor programs, the total area for batteries is very limited thereby restricting the battery capability. Power harvesting has emerged as one of the most abilities options for powering the sensor electronics and has been extensively studied in recent years. Power may likewise be gathered from different encompassing forces sources practically identical to electromagnetic, wind, mechanical vibration, RF, and warm power and so forth. There are three general power sources that use vibration, especially electrostatic, electromagnetic, and piezoelectric among the resources, the piezoelectric vibration founded vigor harvesting method has got an excellent deal of concentration due to the effect of its ease of integration with electronic programs and moderate vigor density. A few rectifier circuits had been stated on piezoelectric transducers have been stated. The circuits intention to curb the voltage drop throughout rectifier diodes while still references go to slash the wasted costs associated with the charging of the plate capacitance of the piezoelectric transducer to additionally expand the general effectivity of the techniques. The specialists have utilized techniques called predisposition flip and synchronized switch reaping on inductor (SSHI) to help the extraction of vitality. Nevertheless, the predominant problems associated with this scheme involve the requirement of relatively big inductor to enhance the overall efficiency while still nonetheless requiring very difficult control circuits.

### Piezoelectric Energy Harvester:

Figure 1. shows a typical building block of a piezoelectric energy harvesting system. A piezoelectric transducer is a device that transforms vibrational energy into electrical energy. Piezoelectric transducer produces an AC output voltage corresponding to the pressure applied at a particular frequency. Accordingly, an AC-to-DC converter or a rectifier is required to obtain a valuable DC form output voltage. By and large, the power produced by the rectifier output is saved in a battery, a great capacitor or is regulated by way of a DC-DC converter, LDO and many others. To power up the masses. There are two forms of traditional rectifiers: voltage-doublers and full-bridge rectifier. The most important drawbacks of a traditional structure are normally low extraction and conversion efficiencies. Low extraction efficiency is caused by using charging and discharging of the inner capacitances of a piezoelectric transducer itself. To overcome this drawback, a number of exclusive schemes had been studied, corresponding to synchronized charge harvesting on inductor, switch-only harvester, etc. To improve the conversion effectivity of a rectifier, a diode may also be replaced by using a CMOS circuit. For illustration, a CMOS cross-coupled rectifier has decrease voltage drop compared to an additional off-chip diode rectifier. To obtain a regulated output voltage, it is fascinating to have a regulator equivalent to DC-DC converter, LDO and many others. However, these regulators with to be had power below 100  $\mu$ W have issues with the energy efficiency because of the related energy consumption of the circuits.

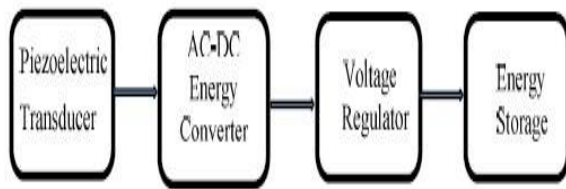


Figure 1: A typical building block of a piezoelectric energy harvester

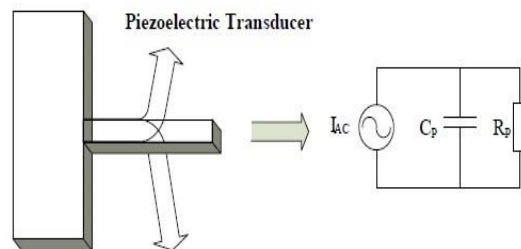


Figure 2(b) Simplified model of a piezoelectric transducer

**I. PREVIOUS WORK**

**A) Modeling**

A piezoelectric device may also be modeled in each mechanical and electrical domains which might be coupled by means of a magnetic coil, as illustrated in figure 2a. Within the mechanical domain model of a piezoelectric transducer,  $R_M$  represents the mechanical damping,  $C_M$  is the reciprocal mechanical stiffness, and  $L_M$  is the effective mass. In the electrical area, the vibrational excitation may also be modeled as a current supply with a parasitic capacitance,  $C_P$  and a parasitic resistance,  $R_P$ . Within the proposed CMOS active rectifier with a switch, a usual model of a piezoelectric device has been used. The parameter values for the piezoelectric transducer are chosen such that  $C_P$  is 25 nF,  $R_P$  is 1 MΩ, and  $I_P$  is 45 μA.

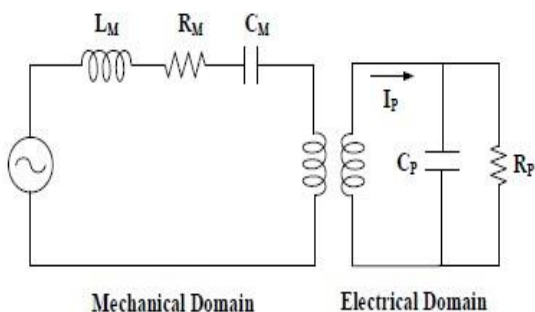


Figure 2(a) Schematic of a piezoelectric transducer showing mechanical to electrical domain

**B) Full Wave Rectifier**

As shown in Fig. 2, a piezoelectric transducer may be modeled as a sinusoidal contemporary supply  $i_P(t) = I_P \sin(2\pi f_P t)$ , in parallel with a parasitic capacitor  $C_P$  and a parasitic resistor,  $R_P$ . The amplitude of a piezoelectric transducer present,  $I_P$  varies with the mechanical excitation dipole moment on the piezoelectric transducer and  $f_P$  is the excitation frequency of a piezoelectric transducer. A ordinary complete-wave bridge rectifier consists of 4 diodes. The disadvantage of utilizing a diode in a rectifier circuit is that it final results in a terrific voltage drop because of the fact that the flip-on voltage,  $V_{on}$  of a diode is extra oftenthannot 0. Five ~ 0.7 V. Fig. 3 depicts conventional passive diode configured full-wave rectifier.

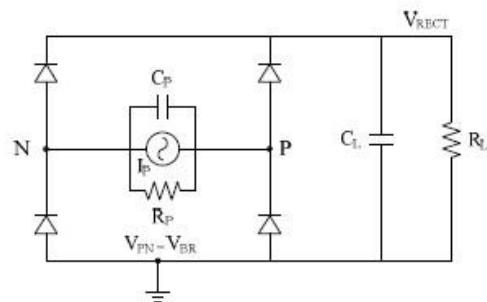


Figure 3: Conventional passive diode full wave rectifier

**C) CMOS Full Wave Rectifier**

To reduce the voltage drop across a diode which impacts the conversion efficiency, a CMOS based full wave bridge rectifier is offered on this paper. By using utilizing a CMOS science as a substitute. In addition, a swap may also be delivered in a piezoelectric transducer circuit to lessen the waste cost which fees or discharges the parasitic capacitance of a piezoelectric device. Fig. 4 shows the circuit diagram of the proposed circuit. Unlike conventional rectifiers, the proposed rectifier is composed of entirely digitally controlled system to minimize current consumption by a control circuit except the low-energy analog CMOS comparator which consumes a static current of 60nA. Power is minimized utilizing an analog circuit when you consider that the essential power consumption of the lots of the control circuits is attributed to the static current consumed by way of the analog circuits.

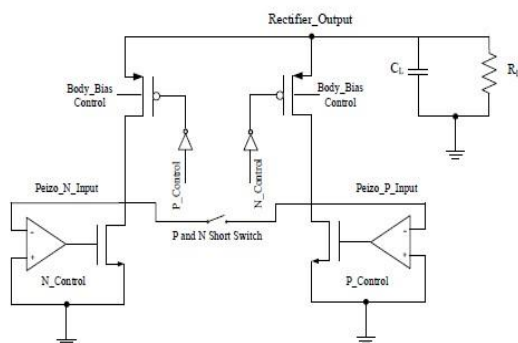


Figure 4: Circuit schematic of the proposed full-wave rectifier

Whenever a piezoelectric transducer is crossing the zero issue, the only-shot control circuit is designed to provide a quick pulse which shorts the input ports of a piezoelectric transducer. This shorting movement will help enhance the efficiency of the piezoelectric energy harvesting with the useful resource of reducing a waste price. The backside two NMOS gadgets are managed with the aid of the usage of a low-strength comparator at the same time as the

pinnacle PMOS gadgets are controlled through P\_Control and N\_Control bar indicators.

## II. PROPOSED METHOD (A) Boost Converter

A boost converter (step-up converter) is a DC-to-DC energy converter which converts input voltage to some higher level voltage (on the identical time as stepping down temporarily) that is step the supply input to its load output. It is a type of switched mode power supply (SMPS) containing at least two semiconductors (a diode and a transistor) and a base one power source detail: a capacitor, inductor, or the two in total. The fundamental working guideline of a boost converter is to reduce the voltage ripple, filters produced from capacitors (once in a while in mixture with inductors) are delivered to the kind of converter's output (load-facet clear out) and input (supply-facet clear out). DC-DC converters with better performance can be achieved through transformer and coupled inductor based topologies, although there are many difficulties involved in designing a transformer for high voltage switching applications. Parasitic capacitance can be a large source of loss in a transformer, and a good dielectric material must be chosen to minimize losses in the parasitic capacitance.

In terms of technical packages, it is required to change a hard and fast voltage DC to convey the correct directly into a variable-voltage DC output. A DC-DC exchanging converter changes over voltages specifically from DC to DC and is effectively known as a DC converter. A DC converter is identical to an AC transformer and is with an always factor turns proportion. A DC converter could be used to step down or step up a DC voltage delivered, like a transformer. DC converters are significantly used in

traction motor which manipulate in electrical vehicles, trolley vehicles, marine hoists, forklifts cars, and mine haulers. They offer sunnecessarystandard general execution, exact speeding up control and rapid dynamic response. They will be equipped for performing in regenerative braking of DC a utostobackward quality yet again into the give. This choice ultimateresultsinenergy economicsavingsfortransformation techniques with regular steps. DC converters are carried out in DC voltage regulators; and are further used, with an inductor in conjunction, to generate a DC supply, especially for the prevailing supply inverter.

Overall performance, length, and rate are the most extreme fundamental advantages of voltage converter even when contrasted with linear converters. The switching power converter efficiencies can keep running among 70-80%, on the similar time as linear converters are with the help of full resource and huge 30% green. The DC-DC Switching up Converter is intended to give an inexperienced approach of taking a given DC voltage converter and boosting it to a useful voltage. In a boost converter, the output voltage is higher than the input voltage—therefore the result will be “boosted”. A boost converter utilizing a power MOSFET is established below.

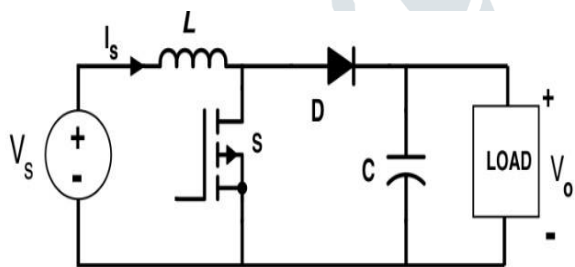


Figure 5: Circuit Diagram Of Boost Converter

**B) Diode Full Wave Rectifier With Boost Converter**

The diode full wave rectifier with boost converter layout is defined in fig 6. In this circuit diagram output voltage may be extended. Electricity dissipation of the circuit further more extended in evaluation to CMOS full wave rectifier with enhancement in voltage as like boost converter.

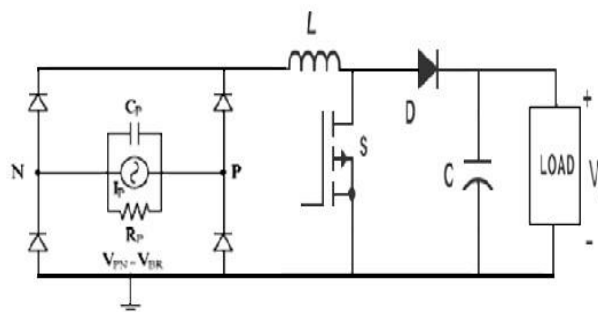


Figure 6: Circuit diagram of Diode rectifier with Boost Converter.

**C) CMOS Full Wave Rectifier With Boost Converter**

A CMOS Full wave rectifier with enhanced boost converter is shown in fig 7. This circuit operation is like a diode rectifier. In this circuit, shorting movement will help in beautifying or boosting the overall performance of the piezoelectric supply harvesting through reduced waste charge. And the boost converter will increase the voltage output at the load.

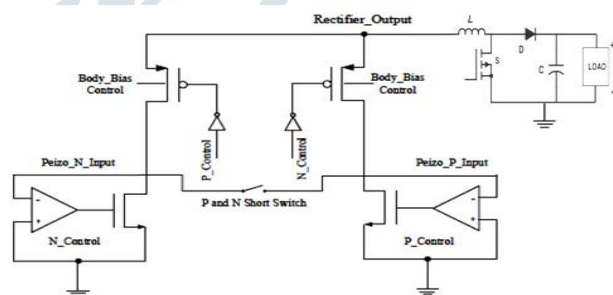
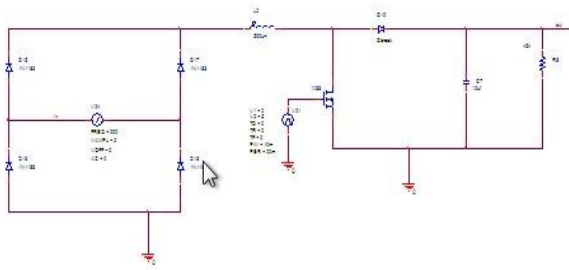


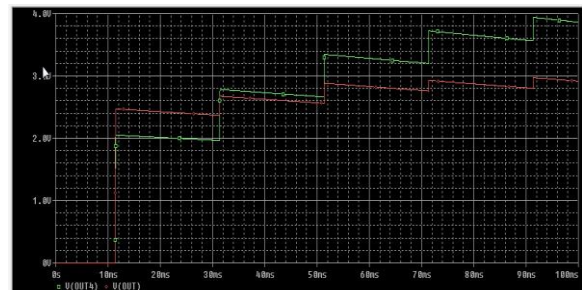
Figure 7: circuit diagram of the CMOS rectifier with boost converter

### III. RESULTS

#### Diode full wave rectifier with boosting:

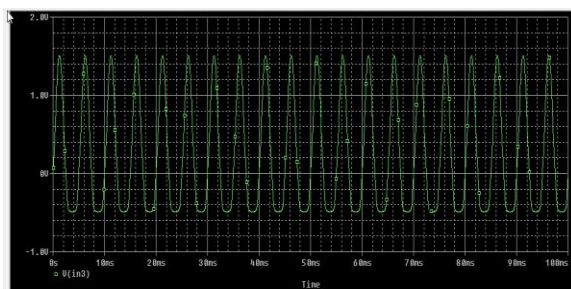


#### Voltage comparison :

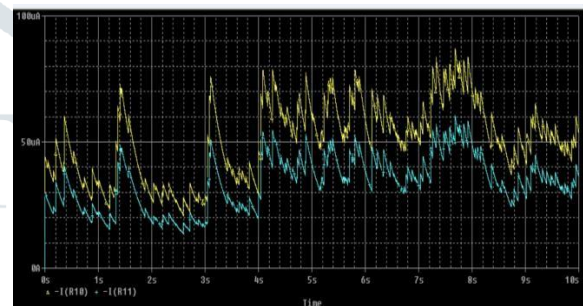


□ Diode Voltage  
□ CMOS Voltage

#### Diode full wave rectifier input:

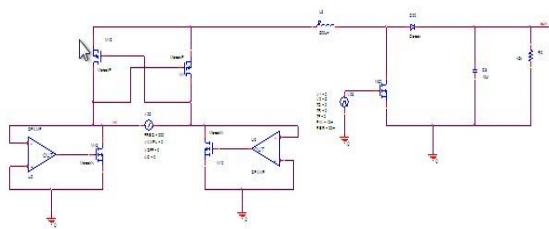


#### Current comparison :

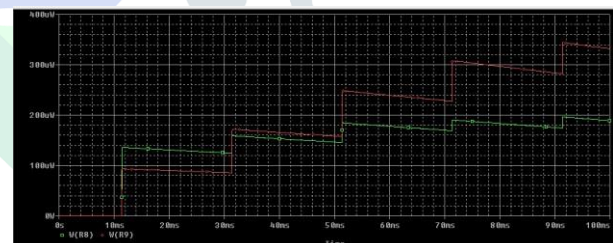


□ Diode Current  
□ CMOS Current

#### CMOS Full wave Rectifier with boosting:

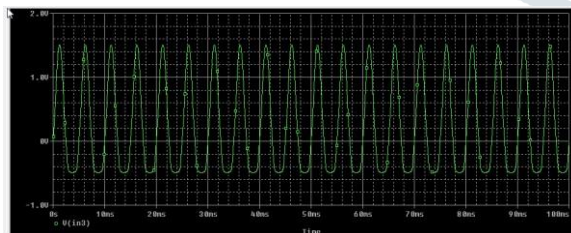


#### Power consumption comparison:



□ Diode Power  
□ CMOS Power

#### CMOS full wave rectifier input:



#### Diode&CMOS rectifier Comparison

Comparison result of diode rectifier and CMOS rectifier.

Table:parameter	Diode	CMOS	Voltage(V)
Power(uW)	195	340	3.04.0
Current(uA)	6388		
Amplitude(v)	22		
Frequency(Hz)	200	200	

#### IV. CONCLUSION

This paper the implementations and simulation are done and will also be accomplished in P-spice capture tool. In this paper, The proposed process is to scale down the power dissipation and examine with the previous diode full wave rectifier with boost converter. The amplitude of the output voltage is 36 V. The simulated conversion ratio of  $V_{OUT}$  versus  $V_{IN}$  is 80.2% whereas calculated worth is fifty eight%. P.O.U.T of the proposed full-wave rectifier with raise converter is  $5.1 \mu W$ , and the efficiency of the method is 45%. The simulated and the measured values were not intentionally matched seeing that of the obstacle of the dimension tools. The input of the proposed process used to be furnished with the aid of a perform generator as an alternative of an actuator to shake a piezoelectric transducer. The proposed full wave rectifier with boost converter can retain a waste charge of naturally aspired piezoelectric device.

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