Power Quality Improvement Using Unified Power Quality Conditioner (UPQC)

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Abstract: The advance use of power electronic devices introduces harmonics in the supply system which creates a problem in the quality of power delivered. Good Power Quality is very much important for our day to day use of appliances in both industrial and domestic sectors. Researchers have tried and implemented many useful technology for removing all the voltage and current related harmonic occurrence problems which in turn improves the quality of power delivered to the power system. The prime focus of this paper is the implementation of control strategies like SRF theory and instantaneous power (p-q) for the operation of Unified Power Quality Conditioner (UPQC) which is one of the recent technology that includes both series and shunt active power filter operating at the same time and there by improves all the current and voltage related problem like voltage sag/swell, flicker, etc. at the same time and helps in reduction of Total Harmonic Distortion (THD). In this paper it is shown via MATLAB simulation how UPQC model can be used to decrease the % THD in source voltage, source current and load voltage waveforms created due to non-linear sensitive loads usage.

IndexTerms – Power Electronic Devices, Harmonic occurrence, Unified Power Quality Conditioner, Harmonic Distortion

I. INTRODUCTION

1.1 Background

In the present scenario non-linear loads have become extremely important and people are becoming dependent on it. Few of these non-linear loads are televisions, printing and fax machines, rectifiers, inverters, speed drives, AC, etc. Harmonics are introduced in the lines due to the extensive use of these loads in our everyday purpose. The stability of any electrical devices depends on its voltage and current waveforms. If the fundamental waveform is sinusoidal, and its harmonics are sinusoidal too then these harmonics occurs in integral multiples of the fundamental waveform. Due to these harmonic distortion created by nonlinear loads several problems are caused in the appliances used in our purpose like: motor getting overheated, increase in several types of losses, permanent damage of equipment in the worst case, high error in meter reading, etc. Hence removal of these harmonics or harmonic mitigation from voltage and current waveforms are of great concern for electrical engineers.

1.2 Power Quality (PQ) Problems

The voltage quality which a consumer gets for operation of load or given from some particular utility very important. PQ problem deals with deviation of voltage/current from their ideal sinusoidal waveforms. The power quality became mainly poor at those typical locations where we connect the loads in the grid. Power Quality has its various definitions and importance as per the its usage by which we define them in the process. From designer perspective, PQ is defined as that there should be no variation in voltage and there should be complete absence of noise generated in grounding system.

Voltage Sag

Voltage Sag is the decrease in rms voltage of power frequency for a time span of half cycles to 1 minute. Voltage sag is a severe and drastic PQ issue especially with sensitive loads which are voltage sensitive like equipment for control processing, adjustable speed drives (ASD) and computers.

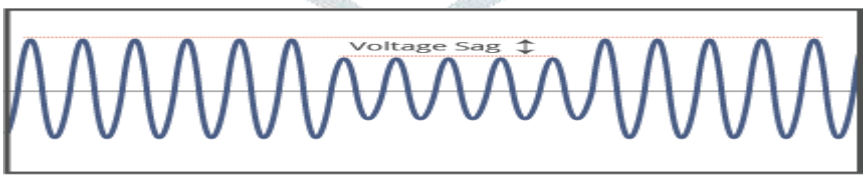


Fig. 1.1 Voltage sag found in supply voltage

Voltage Swell

Voltage swell is a sudden increase in the rms supply voltage varying in a range from 1.1p.u. To 1.7 p.u., with a approximate time range of from half a cycle to 1 min. These appear due to large loads sudden shutdown, capacitor banks getting energized, or due to few faults produced inside the power system. Its occurrence probability appear when compared to voltage sags is very much less, but these are more harmful to sensitive equipment/non-linear loads.

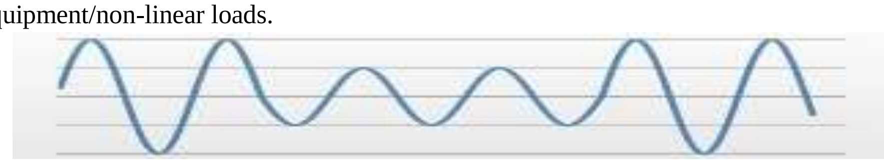


Fig. 1.2 Voltage swell found in supply voltage

1.3 Active Power Filters

APF's are the electrical equipment which are connected sometimes as series model or shunt model and sometimes as a combination of both series and shunt filters. UPQC is a model where both series and shunt APF connected via a common dc link capacitor are implemented in one circuit only and they help to solve all voltage and current harmonics problems simultaneously.

1.4 Literature Review

In [1] a new Synchronous-Reference- Frame (SRF)-based control method to compensate power-quality (PQ) problems through a three-phase four-wire unified PQ conditioner (UPQC) under unbalanced and distorted load conditions. In [2] it is shown to construct an APF with hysteresis current control method. A simple proportional-integral(PI) controller is brought in use in order after to regulating the average dc bus voltage which thereby make the reference supply current peak value and supply voltage in phase and the model is tested with different linear and nonlinear loads to remove the harmonics and reduce reactive power. In [3] the technology based on unit vector template generation from distorted input supply is used for solving problems related with voltage and current harmonics in a basic UPQC model. H.Akagi et al. [4] proposed the instantaneous active and reactive power concept. It describe an instantaneous reactive power compensators that doesn't uses a energy storage device but switching devices. It proved that both harmonic currents and fundamental reactive power in transient states can be removed. We understand the advanced control strategy i.e d-q-o method for compensating the voltage harmonics and hence the voltage signal at series active filter is utilized to find the reference signal for the parallel active filter using p-q theory. Metin Kesler [5] proposed an advanced control method (SRF) to overcome the problems of power quality through a three-phase UPQC under unbalanced load conditions. Its performance was analyzed. The proposed control system helps in improving the power quality at the point of common coupling (PCC) on power distribution system under unbalanced load conditions and non- ideal mains voltage by compensating the current and voltage harmonics and the reactive power.

II. Single Phase Shunt Active Power Filter

2.1 Introduction

In industries and domestic usage we are having large numbers of single phase loads which employs solid state control which requires the attention to the problem of harmonics occurring due to its usage. These solid state controllers try to convert and also control ac power fed to many loads and thereby increase efficiency of the system and in this process they also introduce harmonic components in the lines which create several problem which need to be solved. A simple figure to depict the operation of single phase APF is shown below:

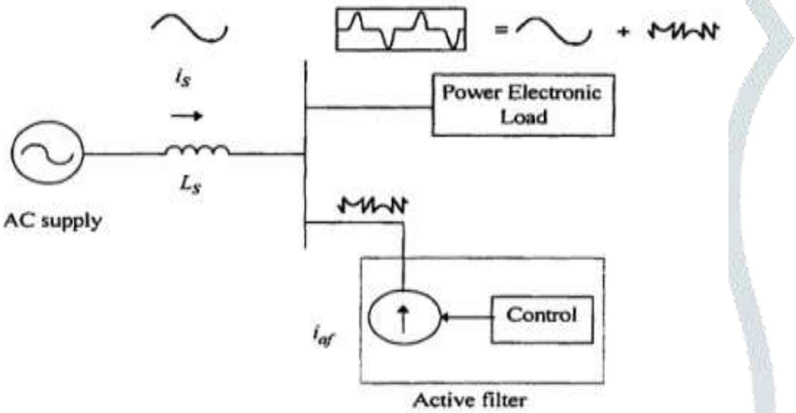


Fig 2.1 Principle of Single phase shunt active power filter

2.2 Design of the system

The idea used here is to produce harmonic current having components which has 180° phase shift to the components of harmonic current which are generated by the use of nonlinear loads. The concept is totally based on injecting harmonic current in the ac system similar in amplitude but opposite in phase when compared with load current waveform harmonics. In normal conditions, the source is assumed as a perfect sinusoidal voltage i.e

$$V_s\,(t) = V_m \sin(\omega t) \qquad (2.1)$$

The reference source current and actual source current is the passed via a hysteresis carrier less PWM current controller to achieve the gating signals for the MOSFETs operation which has been used in the APF.

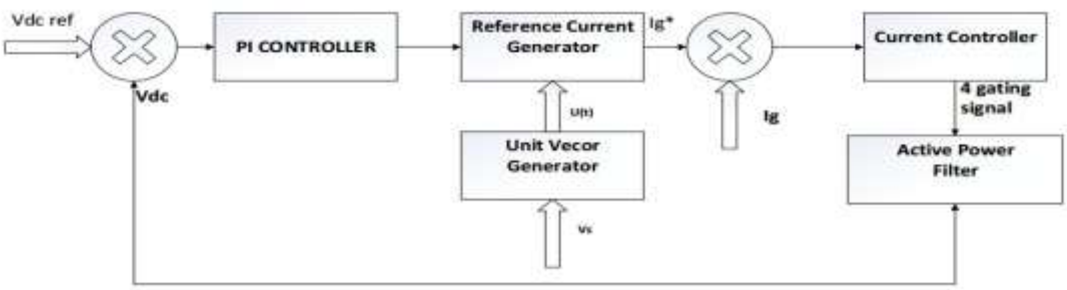


Fig 2.2: Unit vector Control scheme for shunt APF

In simple words from Fig 2.2 we can say that in order to run the Shunt APF and achieve the above mentioned task the voltage across the dc link is sensed and compared with the reference dc link voltage. This error is then processed by a PI controller. The resultant signal from PI controller is then multiplied with unit vector templates of equation (2.9) giving reference source current signals. The actual source current must be equal to this reference signal. In order to follow this reference current signal, the three phase source current is also sensed and compared with above calculated reference current signals. The error generated is then processed by a hysteresis current controller with a definite particular range of band, generating gating signals for shunt APF.

III. PQ Theory & Analysis

3.1 Introduction

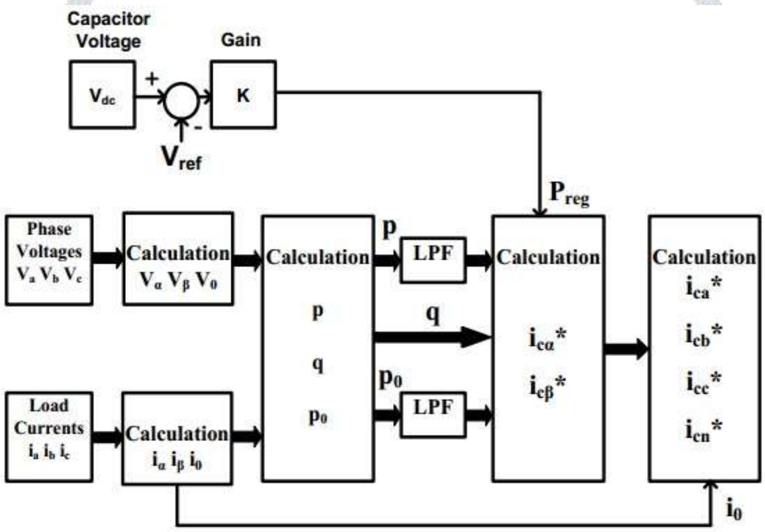
The standards in Power quality (IEEE-519) has compelled the engineers for limiting the total harmonic distortion (THD) to an acceptable range which is mostly caused due to daily and regular usage of power electronic devices in industries and domestic appliances. The total harmonic distortion, or THD, of a signal is a measurement of the harmonic distortion present and is defined as the ratio of the sum of the powers of all harmonic components to the power of the fundamental frequency. Mathematically it is given as

$$\text{THD \%} = 100 \text{ X } \sqrt{\sum_{N\neq 1}^{\infty} \frac{I_{sn}^2}{I_{s1}^2}} \quad (3.1)$$

Instantaneous power theory or p-q theory is useful for the analysis of both transient-state and steady state. In this method the commanding or driving signals required for filter operation is obtained from instantaneous active and reactive power and hence there is no need of phase synchronization of phase.

3.2 Instantaneous Power Theory

In [9] H.Akagi has defined a theory on the basis of instantaneous power in three phase system either in the presence or absence of neutral wire. This p-q approach is valid for operation under all conditions namely transient and steady state operation. This theory makes use of some famous transformation models defined like Clarkes Transformation. Here the voltage and current waveforms are sensed and then made to transform from a-b-c coordinates to α− β−0 coordinates. After this transformation, based on a certain set of equation we calculate active and reactive power and then eliminate the power components having harmonics in it by passing through a certain suitable low pass filter of suitable frequency. This new set of power and already derived new voltages in a different coordinate namely α− β−0 coordinates ,we again find out the reference source current in this frame only and then using Inverse Clarkes Transformation we convert this reference source current again back to a-b-c coordinates. A simple block diagram explaining the complete operation of this important p-q theory is given below



3.3 Analysis of P-Q Approach

Clarke's transformation needed for converting source voltage and current from a-b-c to $\alpha - \beta - 0$ coordinate is given by following matrix:

$$\begin{bmatrix} V_{0s} \\ V_{\propto s} \\ V_{\beta s} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} V_{sa} \\ V_{sb} \\ V_{sc} \end{bmatrix} \quad (3.2)$$

Similarly current transformation is

$$\begin{bmatrix} i_{0s} \\ i_{\propto s} \\ i_{\beta s} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} i_{sa} \\ i_{sb} \\ i_{sc} \end{bmatrix} \quad (3.3)$$

3-Phase instantaneous power is given by

$$\begin{aligned} P_{3-\phi} &= V_{sa} i_a + V_{sb} i_a + V_{sc} i_c = V_{\alpha s} i_{\alpha s} + V_{\beta s} i_{\alpha s} + V_{0s} i_{0s} \\ &= P_a(t) + P_b(t) + P_c(t) = P_{\alpha s}(t) + P_{\beta s}(t) + P_{0s}(t) \\ &= P_r(t) + P_{0s}(t) \end{aligned} \quad (3.4)$$

Here we define $p_r(t) = p(t) + p_{\beta s}(t)$ as instantaneous real power & $p_{os}(t) = p_{os}(t)$ as inst. Power of zero sequence. Here we can note down an important benefit of this transformation in which separation of system zero sequence component is easily done.

The active (P_s) and reactive power (Q_s) is then calculated by the following equations:-

$$\begin{bmatrix} P_s \\ Q_s \end{bmatrix} = \begin{bmatrix} V_{\alpha s} & V_{\beta s} \\ -V_{\beta s} & V_{\alpha s} \end{bmatrix} \begin{bmatrix} i_{\alpha s} \\ i_{\beta s} \end{bmatrix} \quad (3.5)$$

Hence from above matrix we can write $Q_s = V_{\alpha s} i_{\beta s} - V_{\beta s}\, i_{\alpha s}$. In terms of a-b-c components Qs is written as:-

$$Q_s = \frac{\left[(V_{sa} - V_{sb})i_{sc} + (V_{sb} - V_{sc})i_{sa} + (V_{sc} - V_{sa})i_{sb}\right]}{\sqrt{3}} \quad (3.6)$$

$$\begin{bmatrix} i_{\alpha s} \\ i_{\beta s} \end{bmatrix} = \frac{1}{\Delta}\begin{bmatrix} V_{\alpha s} & -V_{\beta s} \\ V_{\beta s} & V_{\alpha s} \end{bmatrix}\begin{bmatrix} P_s \\ Q_s \end{bmatrix} \quad (3.7)$$

Where $\Delta = V_{\alpha s}^2 + V_{\beta s}^2$

On separating components of active and reactive current by 3.7 we can write

$$\begin{bmatrix} i_{\alpha s} \\ i_{\beta s} \end{bmatrix} = \frac{1}{\Delta}\left\{\begin{bmatrix} V_{\alpha s} & -V_{\beta s} \\ V_{\beta s} & V_{\alpha s} \end{bmatrix}\begin{bmatrix} P_s \\ 0 \end{bmatrix} + \begin{bmatrix} V_{\alpha s} & -V_{\beta s} \\ V_{\beta s} & V_{\alpha s} \end{bmatrix}\begin{bmatrix} 0 \\ Q_s \end{bmatrix}\right\} = \begin{bmatrix} i_{\alpha p} \\ i_{\beta p} \end{bmatrix} + \begin{bmatrix} i_{\alpha q} \\ i_{\beta q} \end{bmatrix} \quad (3.8)$$

Where, $i_{\alpha p} = \frac{V_{\alpha s}P_s}{\Delta}$, $i_{\beta p} = \frac{V_{\beta s}P_s}{\Delta}$, $i_{\alpha q} = \frac{-V_{\beta s}Q_s}{\Delta}$, $i_{\beta q} = \frac{V_{\alpha s}Q_s}{\Delta}$ (3.9)

Now we can find power in $\alpha \,\&\, \beta$ phase's separately as

$$\begin{bmatrix} P_\alpha \\ Q_\beta \end{bmatrix} = \begin{bmatrix} V_{\alpha s} & -i_{\alpha p} \\ V_{\beta s} & i_{\beta p} \end{bmatrix}\begin{bmatrix} V_{\alpha s} & -i_{\alpha q} \\ V_{\beta s} & i_{\beta q} \end{bmatrix} = \begin{bmatrix} P_{\alpha p} \\ P_{\beta p} \end{bmatrix} + \begin{bmatrix} P_{\alpha q} \\ P_{\beta q} \end{bmatrix} \quad (3.10)$$

Where,

$$P_{\alpha p} = \frac{V_{\alpha s}^2 P_s}{\Delta}, \; P_{\beta p} = \frac{V_{\beta s}^2 P_s}{\Delta}, \; P_{\alpha q} = \frac{-V_{\alpha s}V_{\beta s}Q_s}{\Delta}, \; P_{\beta q} = \frac{V_{\alpha s}V_{\beta s}Q_s}{\Delta} \quad (3.11)$$

Hence, 3-Phase Active Power is given by

$$P_{3-\phi}(t) = P_\alpha + P_\beta + P_{0s} = P_{\alpha p} + P_{\alpha q} + P_{\beta p} + P_{\beta q} + P_{0s} = P_{\alpha p} + P_{\beta p} + P_{0s} \quad (3.12)$$

3.4 Compensation Strategy

In order to compensate $P_{\alpha q}$ & $P_{\alpha q}$ by which $P_{\alpha q} + P_{\beta q} = 0$, the filter is injecting compensating current namely $i_{\alpha c}$ & $i_{\beta c}$ to reactive current such that

$$i_{\alpha c} = i_{\alpha q} \;\&\; i_{\beta c} = i_{\beta q}$$

The current $i_{\alpha c}$ is providing the power $P_{\alpha q}$ and $i_{\beta c}$ is providing the component $P_{\beta q}$ as given in eqn. 3.11. So the voltage $V_{\alpha s}$ & $V_{\beta s}$ need to provide only $P_{\alpha p}$ and $P_{\beta p}$. It can also be noted that from (3.12), the power necessary to compensate for $i\alpha q$ is equal to the negative of the power necessary to compensate for $i_{\beta q}$.

The current sources $i_{\alpha c}$ and $i_{\beta c}$ is representing APF, which is generated from the VSI inverter & they are controlled accordingly to produce $i_{\alpha q}$ and $i_{\beta q}$. Hence no source DC is necessary and no large energy storage element is essential for compensating the reactive powers. The reactive power required by one phase is instantaneously supplied by the other phase. Hence size of capacitor is not depending on the amount of reactive power which needs to be compensated.

IV. Synchronous Reference Frame Control of UPQC

4.1 Introduction

SRF controlling method for the operation of UPQC model is very similar to instantaneous reactive power theory method. A major feature this algorithm pursues is that only load current is essential here for the generation of reference current and hence disturbances present in source or distortions present in voltage have will leave no negative impact to the performance of the designed UPQC system. In the given proposed SRF method for UPQC we have optimized the system without using transformer voltage, load, and filter current measurement, .This reduces numbers of measurements are and thereby improving system performance.

4.2 I_d & I_q Components Definition

From the proposed SRF theory "*d*" coordinate component of current namely, is corresponding to positive-sequence and this component is always in phase with voltage. The "*q*" coordinate component of current namely iq is found to be perpendicular to the id component of the current, this $iq$is called negative sequence reactive current. The "*0*" coordinate component of current is found to be orthogonal to both id & iq and we name it as zero sequence component of the current. If iq is found to be negative, the load will be pursuing inductive reactive power and if it is positive, then it will be having a capacitive reactive power. In the proposed nonlinear power systems, id & iq components will have both oscillating components ($\tilde{id}$ & $\tilde{iq}$) and average components ($\bar{id}$ & $\bar{iq}$), as mentioned in the below equations.

$$id = \bar{id} + \tilde{id} \;\&\; iq = \bar{iq} + \tilde{iq} \quad (4.1)$$

In both the coordinates the oscillating part responds to oscillating component & the average part responds to active current($\bar{id}$) and reactive current ($\bar{iq}$).Hence wherever APF applications are made in operation our objective will be to separate the fundamental positive sequence component so that harmonics can be eliminated or removed.

4.3 Modified Phase Locked Loop

For high distortion and system with more unbalance the conventional PLL will give low performance and the transformation angle (ωt) will not vary perfectly linearly with time as desired. A modified PLL can be used under those highly distorted situation under which UPQC filtering operation and results can be improved to a better quality. A simple schematic structure to design modified PLL is shown below:-

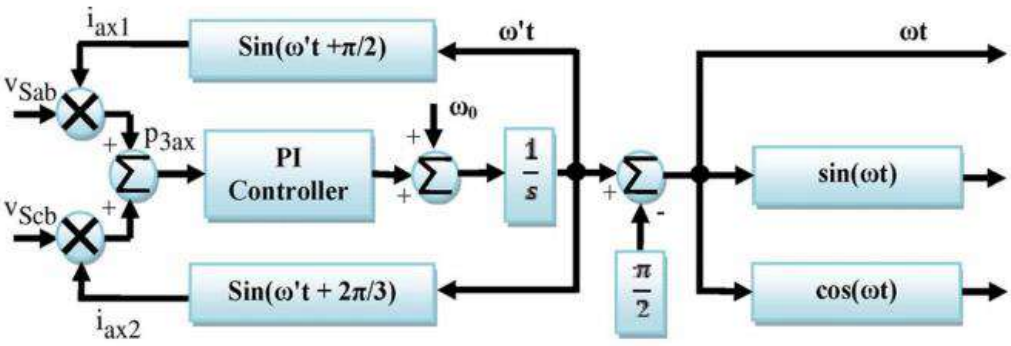


Fig 4.1 PLL block diagram

First we calculate the 3-∅ instantaneous source line voltages $Vsab$ & $Vscb$. This measured line voltages is multiplied with auxiliary ($iax1$ & $iax2$) feedback currents of unity amplitude, in which one will lead leads 120° from the other to achieve auxiliary instantaneous active power ($p3ax$). This is passed through a P-I controller. The referred fundamental angular frequency ($\omega 0 = 2\pi f$) is added to result of P-I controller for the purpose to stabilize output. The result is then passed through an integrator block to get auxiliary transformation angle (ωt).The resultant produced ωt leads 90° to system's fundamental frequency; and hence −90° is added to integrator output for getting system fundamental frequency. When this instantaneous power $p3ax$ reaches zero or gets low frequency oscillation then PLL is said to reach a stable operating point. Also the output ωt will reach fundamental positive sequence component of lien voltage.

4.4 Reference-Voltage Signal Generation for Series APF

The control algorithm for series APF in UPQC model involves the calculations of reference voltage which has to be injected by the series transformer which it performs by comparing the component of positive sequence of source voltage with the load voltages. The supply voltage is sensed and then it is transformed into *d-q-0* frame of reference by the following transformation matrix

$$\begin{bmatrix} V_{s0} \\ V_{sd} \\ V_{sq} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ \sin(\omega t) & \sin(\omega t - 120°) & \sin(\omega t + 120°) \\ \cos(\omega t) & \cos(\omega t - 120°) & \cos(\omega t + 120°) \end{bmatrix} \begin{bmatrix} V_{sa} \\ V_{sb} \\ V_{sc} \end{bmatrix} \quad (4.2)$$

Hence we can say that $Vsd = \overline{Vsd} + \widetilde{Vsd}$ (4.3)

The harmonic part is separated by passing the d-component voltage Vsd via LPF. The output of this LPF is only the average component $\overline{Vsd}$. The zero and negative components namely V_{sq} & V_{s0} of source voltage is terminated or made to zero for compensating harmonics of load voltage, and unbalance. The reference load voltage is calculated by passing the new set of components of d-q-0 frame via a inverse transformation which converts it again to the original a-b-c reference frame.

This inverse transformation called Inverse Parks transformation is shown below:-

$$\begin{bmatrix} V_{la^*} \\ V_{lb^*} \\ V_{lc^*} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & \sin(\omega t) & \cos(\omega t) \\ \frac{1}{\sqrt{2}} & \sin(\omega t - 120°) & \cos(\omega t - 120°) \\ \frac{1}{\sqrt{2}} & \sin(\omega t + 120°) & \cos(\omega t + 120°) \end{bmatrix} \begin{bmatrix} 0 \\ \overline{V_{sd}} \\ 0 \end{bmatrix} \quad (4.3)$$

The resultant reference voltages as above (V^*_{la}, V^*_{lb}, & V^*_{l}) and actual sensed load voltages (V_{la} , V_{lb} & V_{lc}) are compared and then passed via a sinusoidal pulse width modulation(PWM) for controlling switching or gate signals for the series filter operation of IGBT used and to fight against and remove all problems related with voltage as discussed in chapter 1 namely, harmonics in voltage, sag/swell, voltage unbalance at the PCC. The whole idea of generating reference voltage for series APF operation in UPQC model is depicted below:-

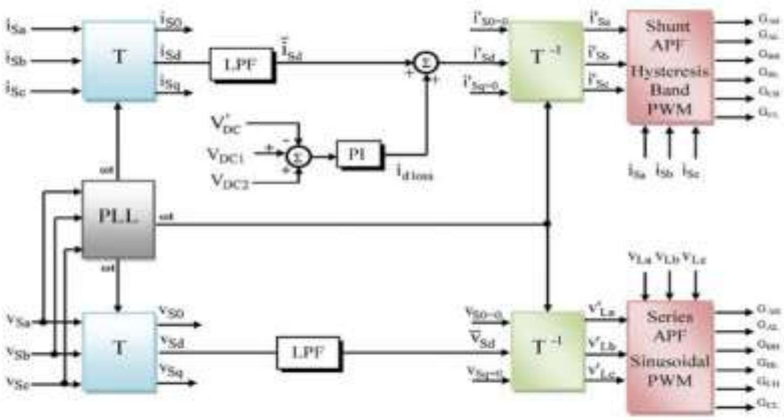


Fig 4.2 SRF control for UPQC operation

4.5 Reference-Source-Current Signal Generation for Shunt APF

The shunt APF as discussed in chapter 2 is useful for avoiding the problems related with the current harmonics generated in our UPQC model with nonlinear load and also takes care for reactive power compensation.

V. MATLAB Simulation and Result

Single phase shunt active filter the Simulink diagram with the said control strategy is given below.

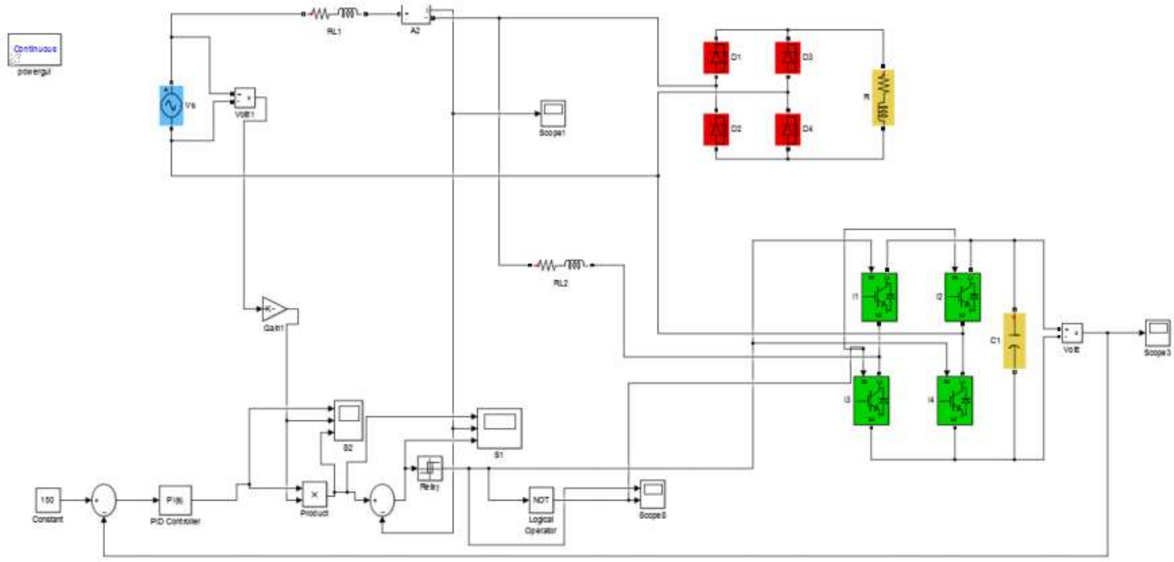


Fig 5.1: MATLAB simulation of single phase shunt APF

System Parameters:
Supply voltage (single phase): 165 volt; Frequency: 50Hz, DC capacitor: $2000\mu F$, Source Rs= 1ohm & Ls=25mH
Filter parameters: R=0.5ohm & L=2.4mH. Non-linear rectifier load: $R1$= 10ohm & $L1$ =100mH.
The results of the simulation model for source current with and without shunt APF are shown below:

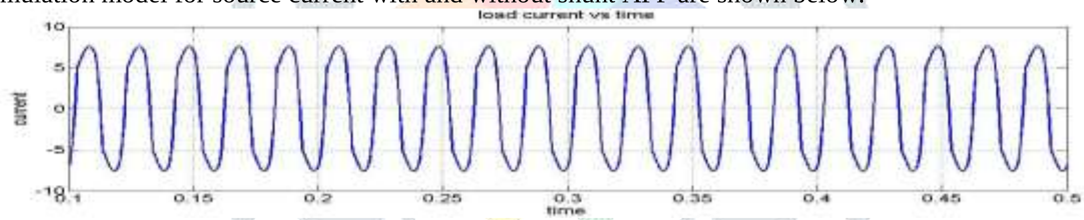


Fig 5.2 Load current without shunt APF

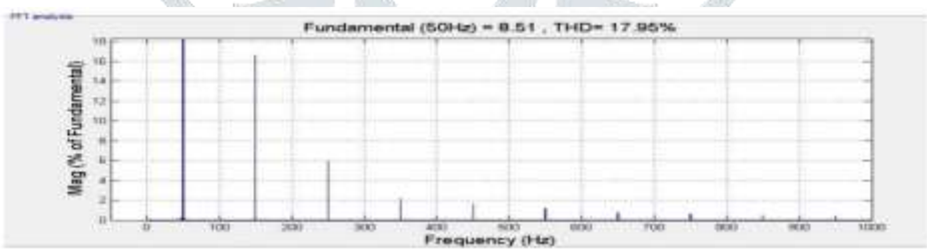


Fig: 5.3 Load current Harmonic Spectrum without shunt APF

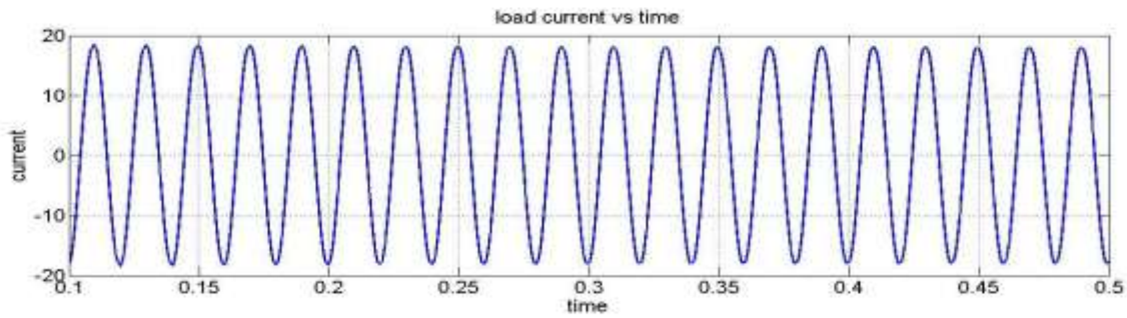


Fig: 5.4 Load current with shunt APF

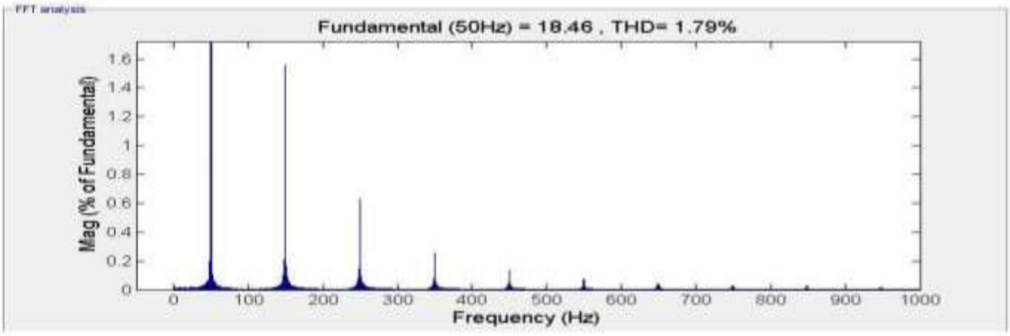


Fig 5.5 Load current Harmonic Spectrum with shunt APF

Discussion

The load current of system with nonlinear load in absence of shunt APF is seen in Fig5.2 and the total harmonic distortion (THD) in load current as shown in Fig 5.3. without the use of shunt active power filter(SAPF) is found to be 17.95% .Now after introducing shunt APF the new improved load current waveform is seen in Fig. 5.4 with the use of shunt active power filter its THD is shown in Fig5.5 & is found to be 1.79% which is within the harmonic limits.

5.2 Result of complete UPQC model with non-linear load

A configuration of UPQC model simulated as per previous discussion.

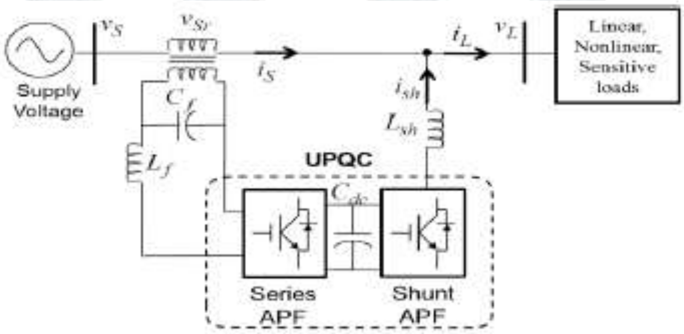


Fig5.6 UPQC model to be simulated

Source voltage- 220V (phase)	Shunt passive filter Parameter: L_{sh}=3.5Mh, R_{sh}=5ohm C_{sh}=4.7 μF
Frequency: 50Hz	$V_{dc\ ref}$ = 500V C=2200 μF
Ls=1mH & R_s= 0.1ohm	Non-linear load: R_{dc}=30ohm L_{dc}=11.5mH
Series Filter inductance L_{se}=1.5mH Series passive filter=R_{se}=5ohm C_{se}=25μF	P-I controller: Kp=1.7 & Ki=0.2

Table 5.1 UPQC Simulation parameters

Before applying the UPQC in the system we sensed the source voltage, source current, load voltage and load current in presence of the non-linear load in our system. Due to the non-linear load we get distortions the supply voltage, current and also load voltage. The waveforms for all the sensed voltages and currents before application of UPQC is shown below for A-phase

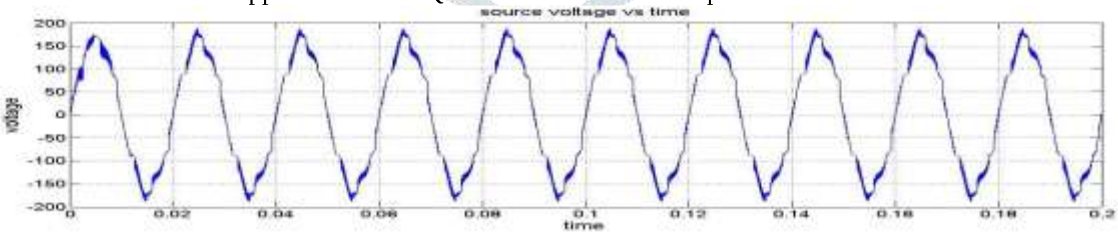


Fig 5.7 : source voltage(a-phase) in non-linear load

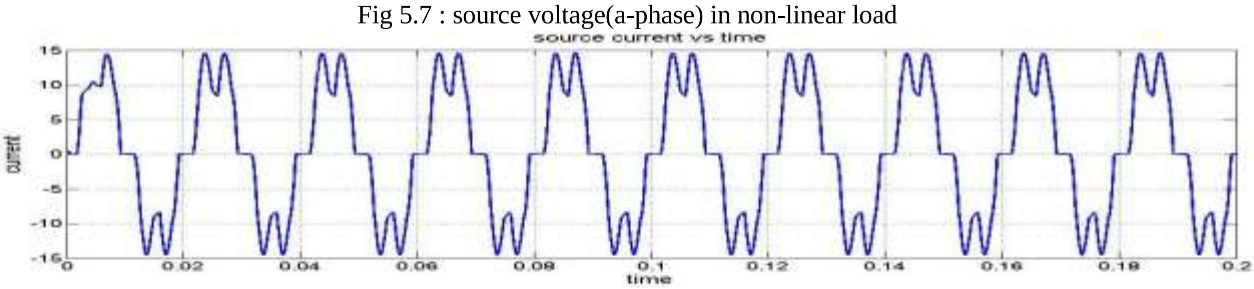


Fig 5.8 :source current(a-phase) in non-linear load

The waveforms obtained after the application of UPQC in the given system compensated the harmonics introduced in the source voltages, source current and load voltage due to the presence of non-linear load. The results of the improved waveform due to UPQC operation for the considered A-phase is shown in the following figure

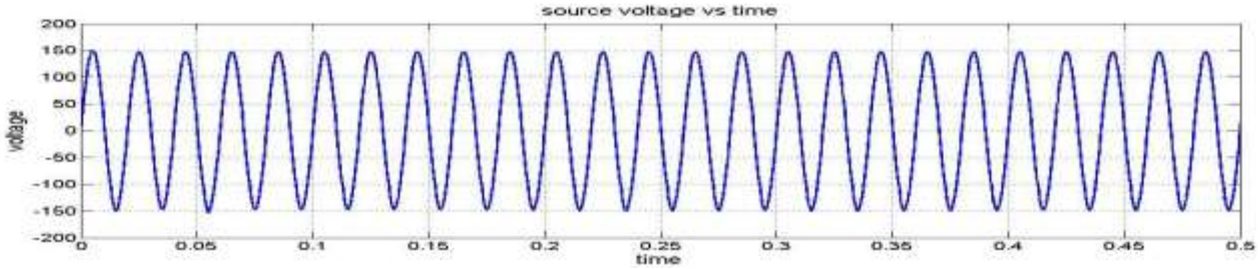


Fig 5.11 source voltage (a-phase) after UPQC compensation in non-linear load

	THD before Compensation	THD after Compensation
Source voltage A-phase	9.36%	2.23%
B-phase	9.16%	2.33%
C-phase	7.85%	2.33%
Source current A-phase	25.68%	3.57%
B-phase	25.78%	3.43%
C-phase	25.71%	3.63%
Load voltage A-phase	13.53%	4.05%
B-phase	13.24%	4.10%
C-phase	11.37%	4.13%

Table 5.2 Comparison of THD before and after UPQC application

CONCLUSION

This paper describes an improved control strategy for the operation of UPQC system. Several control strategy is studied like p-q theory, SRF based approach, unit vector template generation for the APF operation. The UPQC model is simulated in MATLAB using instantaneous power theory. Shunt part of UPQC removes all the current related harmonic problems in the system and series connected APF of UPQC system removes all voltage harmonics which comes up due to the use of nonlinear load. The overall THD is now improved in the system which is clearly observed rom the waveforms and also from Table 5.2 giving the resultant THD before and after UPQC operation.

FUTURE WORK

Preventing the harmonics due to presence of nonlinear load is difficult but its controlling is possible and many research work is still going on for the same. Sliding Mode(SM) and feedback linearization strategy of control is an advanced method for the operation of UPQC due to their ease in implementation and robust in external disturbance. Further dSPACE software which is a good interface between real time hardware and computer, it can be used to implement UPQC model using a further new strategy called Fuzzy control method.

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