

An Improved Step-Up Converter With a Voltage Multiplier Module for a Photovoltaic System

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Abstract: A new voltage multiplier module composed which is having switched capacitors and coupled inductors, with its combination a conventional interleaved boost converter obtains high step-up gain without operating at extreme duty ratio is designed. This proposed converter reduces the current stress and also reduces constrains the input current ripple, which decreases the conduction losses and lengthens the lifetime of the input source. Hence, large voltage spikes across the main switches are reduced, and hence the efficiency will be improved. Even the low voltage stress makes the low-voltage-rated MOSFETs be adopted for reductions of conduction losses and cost.

Keywords: Boost-flyback converter, high step-up, photovoltaic system, voltage multiplier module

I. INTRODUCTION

Nowadays renewable sources of energy are used in worldwide, because of energy shortage and environmental contamination. The high step-up DC-DC converter is widely used in the renewable systems because they produce low voltage output. Among the renewable energy resources the photovoltaic systems having a greater hand in future energy production. The photovoltaic systems transform light energy into electrical energy and convert low voltage into high voltage via step-up converter which can convert energy into electricity using grid-by-grid inverter or store energy into battery set [1]-[2]. A typical photovoltaic system consists of a solar module, a high step-up converter, a charge/discharge controller, a battery set, and an inverter. The high step-up converter performs importantly among the system because the system requires a sufficiently high step-up conversion. The conventional step-up converters, such as the boost converter and flyback converter, cannot achieve a high step-up conversion with high efficiency because of the resistances of elements or leakage inductance. Thus, a modified boost-flyback converter was proposed [3]-[5]. The conventional step-up converters with a single switch are unsuitable for high-power applications given an input large current ripple, which increases conduction losses. Thus, numerous interleaved

structures and some asymmetrical interleaved structures are extensively used. This study also presents an asymmetrical interleaved converter for a high step-up and high-power application [6].

Theoretically, conventional step-up converters, such as the boost converter and flyback converter, cannot achieve a high step-up conversion with high efficiency because of the resistances of elements or leakage inductance. Thus, a modified boost-flyback converter was proposed [18]-[20], and many converters that use the coupled inductor for a considerably high voltage conversion ratio were also proposed [21]-[25]. Despite these advances, conventional step-up converters with a single switch are unsuitable for high-power applications given an input large current ripple, which increases conduction losses. Thus, numerous interleaved structures and some asymmetrical interleaved structures are extensively used. The current study also presents an asymmetrical interleaved converter for a high step-up and high-power application. Modifying a boost-flyback converter, shown in Fig. 2(a), is one of the simple approaches to achieving high step-up gain; this gain is realized via a coupled inductor.

PV Panel: A PV array is a group of several PV cells which are electrically connected in series and parallel circuits to generate the required current and voltage. The characteristics have a current source region and a voltage source region. The impedance is low at voltage source region, whereas high at current source region. Solar radiation is an important factor determining the I-V characteristics, hence these factors are required for the designing and implementation of solar cell. The short circuit current increases with the increase in solar radiation and open circuit voltage decreases with increase in temperature for constant radiation on surface of the cell. Partial shading is a phenomenon occurring in solar cells causing several numbers of maximum power points. The diodes are provided for reducing the reverse currents caused due to the problems of partial shading. These reverse currents cause heating on solar cells, affecting the efficiency and performance at higher temperatures.

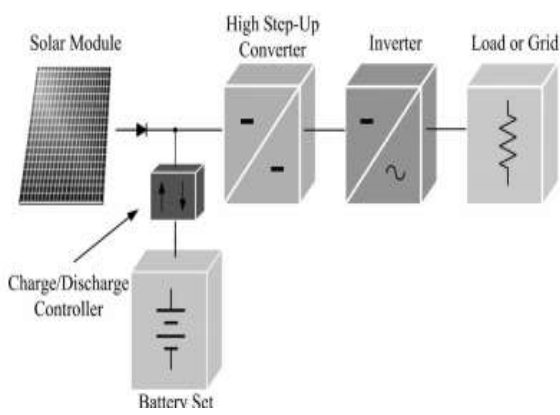


Fig. 1. Typical photovoltaic system.

The electrical system connecting to the photovoltaic system depends on cell temperature, also sudden change in weather condition result in varying solar irradiation on the surface. The output voltage and current varies with the change in these parameters. These will affect in tracking maximum power from the cell.

Maximum Power Point Tracking (MPPT): MPPT technique is to extract maximum power from photovoltaic systems. MPPT maintains the operation of maximum power with efficient MPPT techniques and they differ in many aspects such as required sensors, complexity, cost, range of effectiveness, convergence speed, correct tracking etc. Hill Climbing Techniques are used for tracking maximum power. Here the PV system uses P & O algorithm because of simplicity, less time and parameters requirement. The algorithm starts with setting the reference voltage and power of the module.

Perturbation and Observation method is a Hill climbing method. In this method the operating voltages are increased or decreased for attaining a maximum point of operating and it compares the present value with previous value in each cycle of perturbation. The perturbation value decrease or increase the value of power, then the voltages are adjusted for obtaining a maximum power point. The operating point shifts with the perturbation on voltages, with a step increase or decrease in the duty cycle of the converter connected.

The operating point oscillates around the maximum point causing power loss is a disadvantage of this method of tracking power. The step size is decreased to reduce the oscillation around the operating point. If the power and voltage are increased comparing to the previous cycle, then increase the voltage in order to track maximum power from left of the curve. If the change in power is increased and change in voltage is decreased, then decrease the voltage to track MPP from right of the curve. If the change in power is decreased and change in voltage is increased, then also decrease the voltage to track the operating point back to MPP, which is moving away from maximum point.

Operating principles of converter: The step up converter is a non-isolated topology for boosting low voltage input to high voltage output. The input current is usually continuous in nature and is supplied to the load by either the conduction of diodes or capacitors. The boost converter with voltage multiplier by means of coupled inductor insertion increases the output voltage, hence the voltage gain and efficiency, with low value of duty cycle. The output voltage across the load is the sum of the voltage from boost converter and the voltage across the voltage multiplier capacitors. The required duty cycle can be obtained by adjusting the voltage multiplier, which increases the output voltage. The voltage multiplier module is composed of two coupled inductors and two switched capacitors and is inserted between a conventional interleaved boost converter to form a modified boost-flyback-forward interleaved structure.

When the switches turn off by turn, the phase whose switch is in OFF state performs as a fly back converter and the other phase whose switch is in ON state performs as a forward converter.

II. SYSTEM CONFIGURATION

The step up converter is a non-isolated topology for boosting low voltage input to high voltage output. The input current is usually continuous in nature and is supplied to the load by either the conduction of diodes or capacitors. The boost converter with voltage multiplier by means of coupled inductor insertion increases the output voltage, hence the voltage gain and efficiency, with low value of duty cycle. The output voltage across the load is the sum of the voltage from boost converter and the voltage across the voltage multiplier capacitors.

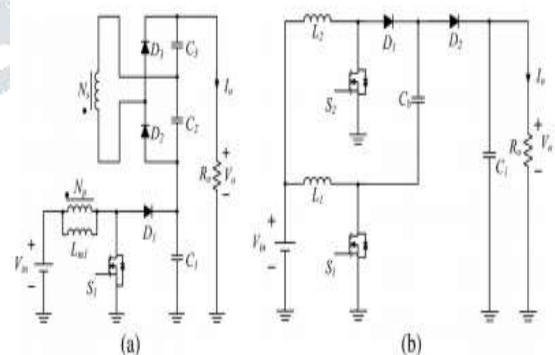


Fig. 2. High step-up techniques based on a classical boost converter. (a) Integrated flyback-boost converter structure. (b) Interleaved boost converter with a voltage-lift capacitor structure

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inserted between a conventional interleaved boost converter to form a modified boost–flyback–forward interleaved structure. When the switches turn off by turn, the phase whose switch is in OFF state performs as a fly back converter, forms.

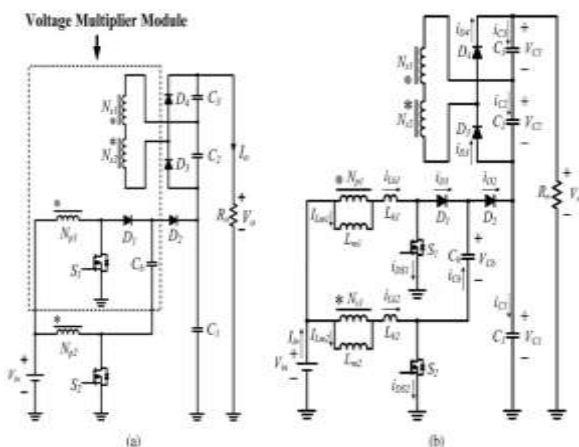


Fig. 3. (a) Proposed high step-up converter with a voltage multiplier module. (b) Equivalent circuit of the proposed converter.

The equivalent circuit of the proposed converter is shown in Fig. 3, where L_{m1} and L_{m2} are the magnetizing inductors; L_{k1} and L_{k2} represent the leakage inductors; L_s represents the series leakage inductors in the secondary side; S_1 and S_2 denote the power switches; C_{c1} and C_{c2} are the switched capacitors; and C_1 , C_2 , and C_3 are the output capacitors. D_{c1} and D_{c2} are the clamp diodes, D_{b1} and D_{b2} represent the output diodes for boost operation with switched capacitors, D_{f1} and D_{f2} represent the output diodes for flyback–forward operation, and n is defined as turn ratio N_s/N_p .

In the circuit analysis, the proposed converter operates in continuous conduction mode (CCM), and the duty cycles of the power switches during steady operation are greater than 0.5 and are interleaved with a 180° phase shift. The key steady waveform in one switching period of the proposed converter contains six modes, of the circuit.

III. SIMULATION RESULTS

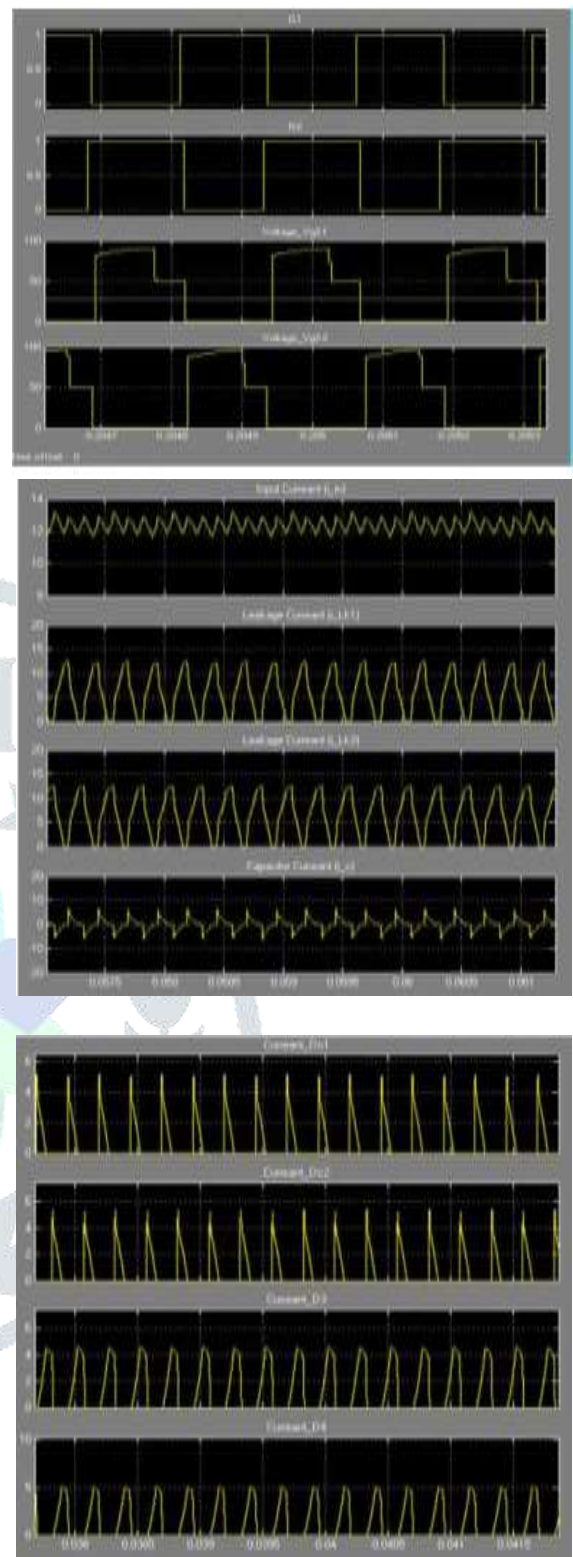


Fig 4 measured waveforms of proposed system

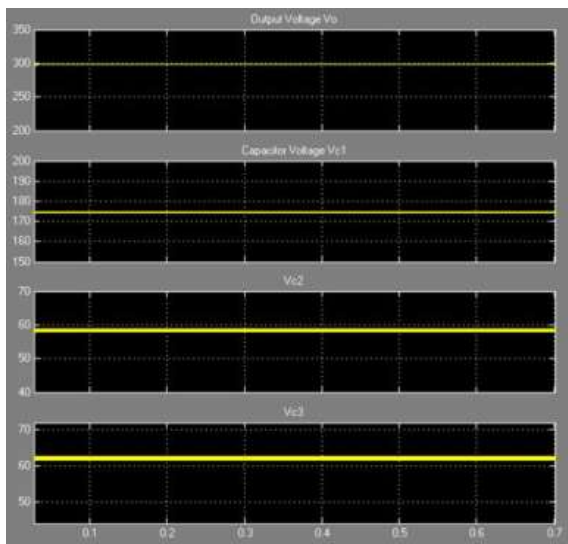


Fig 5 measured voltage waveforms of capacitors

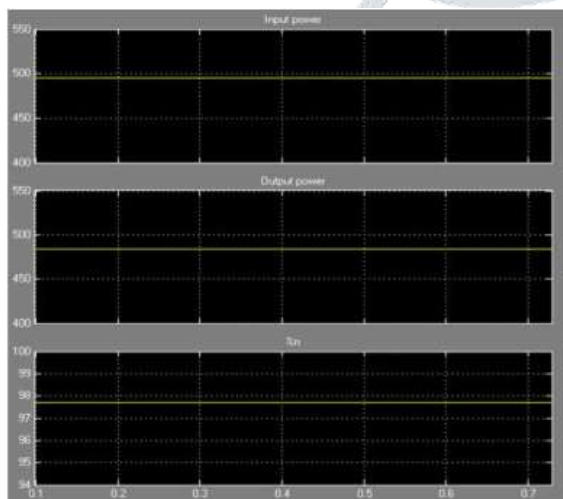


Fig 6 waveforms of input power, output power with efficiency

IV. CONCLUSION

The proposed converter has successfully implemented an efficient high step-up conversion through the voltage multiplier module. The interleaved structure reduces the input current ripple and distributes the current through each component. In addition, the lossless passive clamp function recycles the leakage energy and constrains a large voltage spike across the power switch. Thus the proposed converter is suitable for PV systems or other renewable energy applications that need high step-up high power conversion. full-load efficiency

REFERENCES

- [1] C. Hua, J. Lin, and C. Shen, "Implementation of a DSP-controlled photovoltaic system with peak power tracking," *IEEE Trans. Ind. Electron.*, vol. 45, no. 1, pp. 99–107, Feb. 1998.
- [2] J. M. Carrasco, L. G. Franquelo, J. T. Bialasiewicz, E. Galvan, R. C. P. Guisado, M. A. M. Prats, J. I. Leon, and N.

Moreno-Alfonso, "Power-electronic systems for the grid integration of renewable energy sources: A survey," *IEEE Trans. Ind. Electron.*, vol. 53, no. 4, pp. 1002–1016, Jun. 2006.

[3] J. T. Bialasiewicz, "Renewable energy systems with photovoltaic power generators: Operation and modeling," *IEEE Trans. Ind. Electron.*, vol. 55, no. 7, pp. 2752–2758, Jul. 2008.

[4] Y. Xiong, X. Cheng, Z. J. Shen, C. Mi, H. Wu, and V. K. Garg, "Prognostic and warning system for power-electronic modules in electric, hybrid electric, and fuel-cell vehicles," *IEEE Trans. Ind. Electron.*, vol. 55, no. 6, pp. 2268–2276, Jun. 2008.

[5] F. S. Pai, "An improved utility interface for micro-turbine generation system with stand-alone operation capabilities," *IEEE Trans. Ind. Electron.*, vol. 53, no. 5, pp. 1529–1537, Oct. 2006.

[6] H. Tao, J. L. Duarte, and M. A. M. Hendrix, "Line-interactive UPS using a fuel cell as the primary source," *IEEE Trans. Ind. Electron.*, vol. 55, no. 8, pp. 3012–3021, Aug. 2008.

[7] Z. Jiang and R. A. Dougal, "A compact digitally controlled fuel cell/battery hybrid power source," *IEEE Trans. Ind. Electron.*, vol. 53, no. 4, pp. 1094–1104, Jun. 2006.

[8] G. K. Andersen, C. Klumpner, S. B. Kjaer, and F. Blaabjerg, "A new green power inverter for fuel cells," in *Proc. IEEE 33rd Annu. Power Electron. Spec. Conf.*, 2002, pp. 727–733.

[9] H. Ghoddami and A. Yazdani, "A single-stage three-phase photovoltaic system with enhanced maximum power point tracking capability and increased power rating," *IEEE Trans. Power Del.*, vol. 26, no. 2, pp. 1017–1029, Apr. 2011.

[10] B. Yang, W. Li, Y. Zhao, and X. He, "Design and analysis of a grid-connected photovoltaic power system," *IEEE Trans. Power Electron.*, vol. 25, no. 4, pp. 992–1000, Apr. 2010.

[11] W. Li and X. He, "Review of Nonisolated high-step-up DC/DC converters in photovoltaic grid-connected applications," *IEEE Trans. Ind. Electron.*, vol. 58, no. 4, pp. 1239–1250, Apr. 2011.

[12] A. I. Bratcu, I. Munteanu, S. Bacha, D. Picault, and B. Raison, "Cascaded dc-dc converter photovoltaic systems: Power optimization issues," *IEEE Trans. Ind. Electron.*, vol. 58, no. 2, pp. 403–411, Feb. 2011.

[13] R. J. Wai, W. H. Wang, and C. Y. Lin, "High-performance stand-alone photovoltaic generation system," IEEE Trans. Ind. Electron., vol. 55, no. 1, pp. 240–250, Jan. 2008.

[14] R. J. Wai and W. H. Wang, "Grid-connected photovoltaic generation system," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 55, no. 3, pp. 953–964, Apr. 2008.

[15] L. Gao, R. A. Dougal, S. Liu, and A. P. Iotova, "Parallel-connected solar PV system to address partial and rapidly fluctuating shadow conditions," IEEE Trans. Ind. Electron., vol. 56, no. 5, pp. 1548–1556, May 2009.

[16] G. R. Walker and P. C. Sernia, "Cascaded DC–DC converter connection of photovoltaic modules," IEEE Trans. Power Electron., vol. 19, no. 4, pp. 1130–1139, Jul. 2004.

[17] K. Ujii, T. Izumi, T. Yokoyama, and T. Haneyoshi, "Study on dynamic and static characteristics of photovoltaic cell," in Proc. Power Convers. Conf., Apr. 2–5, 2002, vol. 2, pp. 810–815.

[18] K. C. Tseng and T. J. Liang, "Novel high-efficiency step-up converter," IEE Proc. Elect. Power Appl., vol. 151, no. 2, pp. 182–190, Mar. 2004.

[19] T. J. Liang and K. C. Tseng, "Analysis of integrated boost–flyback step-up converter," IEE Proc. Elect. Power Appl., vol. 152, no. 2, pp. 217–225, Mar. 2005.

[20] J. W. Baek, M. H. Ryoo, T. J. Kim, D. W. Yoo, and J. S. Kim, "High boost converter using voltage multiplier," in Proc. 31st Annu. Conf. IEEE Ind. Electron. Soc., May 2005, pp. 567–572.

[21] R. J. Wai and R. Y. Duan, "High step-up converter with coupled-inductor," IEEE Trans. Power Electron., vol. 20, no. 5, pp. 1025–1035, Sep. 2005.

[22] R. J. Wai, C. Y. Lin, R. Y. Duan, and Y. R. Chang, "High-efficiency DC–DC converter with high voltage gain and reduced switch stress," IEEE Trans. Ind. Electron., vol. 54, no. 1, pp. 354–364, Feb. 2007.

[23] S. K. Changchien, T. J. Liang, J. F. Chen, and L. S. Yang, "Novel high step-up DC–DC converter for fuel cell energy conversion system," IEEE Trans. Ind. Electron., vol. 57, no. 6, pp. 2007–2017, Jun. 2010.

[24] Y. P. Hsieh, J. F. Chen, T. J. Liang, and L. S. Yang, "Novel high step-up dc–dc converter with coupled-inductor and switched-capacitor techniques for a sustainable energy system," IEEE Trans. Power Electron., vol. 26, no. 12, pp. 3481–3490, Dec. 2011.

[25] S. M. Chen, T. J. Liang, L. S. Yang, and J. F. Chen, "A safety enhanced, high step-up dc–dc converter for ac

photovoltaic module application," IEEE Trans. Power Electron., vol. 27, no. 4, pp. 1809–1817, Apr. 2012

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