# SIMULATION AND MODELLING OF 9-LEVEL MLI

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**Abstract:** In this research 9-level MLI modelling and simulation is done. Impact of increasing level of Inverter circuit is also analysied. MATLAB Simulation is done for testing the 9-level inverter circuit for 3<sup>rd</sup> Harmonic injection and its impact evaluation

### **Introduction:**

Multilevel Inverters produces smother output which is very near to sinusoidal waveform. Nine level diode clamped inverter modelling is shown in figure 1. This modelling is done using MATLAB, having 8 switches both in upper and lower leg of the circuit clammed with diode, due to this it is termed as Diode clamped configuration. Figure 2, shows this Diode clamped modelling arrangement.

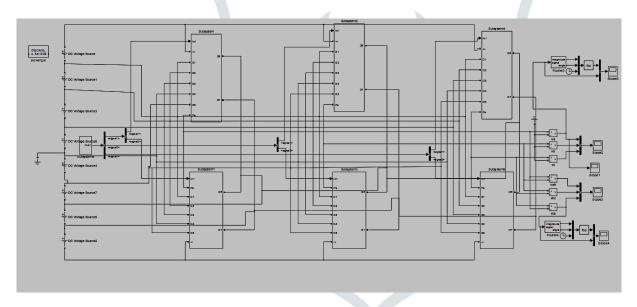


Fig. 1 Simulink model of a 9 level diode clamped MLI

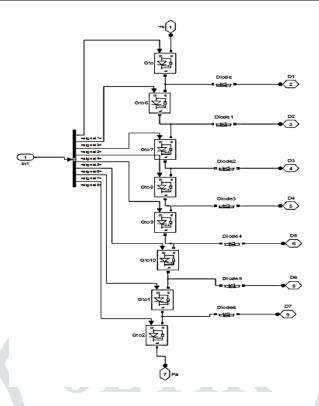


Fig.4.2 subsystem for one leg of 9 level DCMLI

# **Modelling of 9, Level MLI**

Since it is 9-level modelling having eight autonoumsly operated DC voltage source and three branches two having eight switched leg and third one is clamping diode, to generate 9-level in voltage waveform.

Carrier based modulation scheme is utilized for firing the inverter switches, in this carrier wave form is triangular in shape also eight different carrier wave are used. Here third harmonic injection technique is used for reduction of the harmonics in the output, this factor is used here to adjust the switching based on 3<sup>rd</sup> Harmonic to reduce harmonics. Figure 3 shows the subsystem.

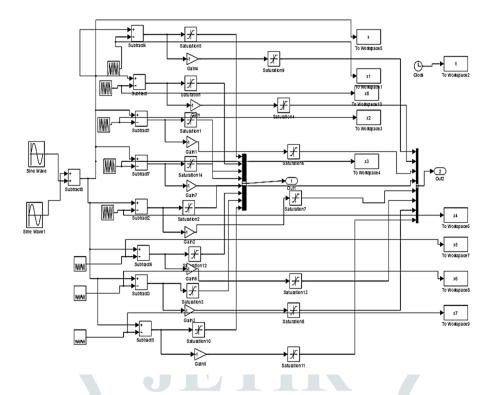


Fig4.3 subsystem for implementation of proposed scheme

Modulation index varies from 0 to 1 with sinusoidal reference waveforms.

Modulation Index = 
$$\frac{f_c}{f_r}$$

Here modulation index is taken as 50. Third harmonic injection is also having variable amplitude this govern the variable change in parameter to be controlled real-time. Here K is magnitude of third harmonic which is injected to the system. Different indexing method which affect the modulation.

- 1. Amplitude modulation index
- 2. Frequency modulation index
- 3. Different value of K.

Figure 4 & 5 shows the impact of 3<sup>rd</sup> harmonic injection at different value of K i.e. at 0.5 & 0.25. Here nine level of waveform is shown as nine steps, where at zero level transition its carrier signal is not present due to this only eight steps are shown.

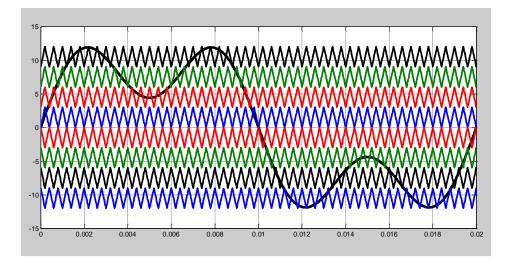


Fig.4. Third harmonic injection modulation scheme for K=0.5

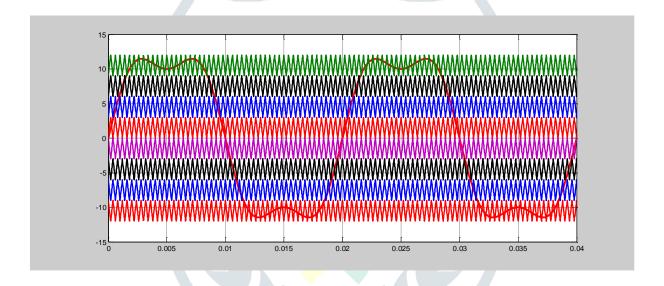


Fig. 5. Third harmonic injection modulation scheme for K=0.25

## **Conclusions**

This research work present simulation and modelling of 9 level diode clamped MLI. Also with different value of K output waveform is shown. Thus 3<sup>rd</sup> Harmonic injection impact is also investigated.

### REFERENCES

- [1] Pregelj, A.; Begovic, M.; Rohatgi, A. "Impactt of inverter configuration on PV system reliability and energy production," Photovoltaic Specialists Conference, 2002. Conference Record of the Twenty-Ninth IEEE, vol., no., pp. 1388-1391, 19-24 May 2002
- [2] Jih-Sheng Lai; Fang ZhengPeng; "Multilevel converters-a new breed of power converters," Industry Applications Conference, 1995. Thirtieth IAS Annual Meeting,

- IAS '95., Conference Record of the 1995 IEEE , vol.3, no., pp.2348-2356 vol.3, 8-12 Oct 1995.
- [3] Rodriguez, J.; Jih-Sheng Lai; Fang ZhengPeng; "Multilevel inverters: a survey of topologies, controls, and applications," Industrial Electronics, IEEE Transactions on, vol.49, no.4, pp. 724- 738, Aug 2002.
- [4] Rodriguez, J.; Franquelo, L.G.; Kouro, S.; Leon, J.I.; Portillo, R.C.; Prats, M.A.M.; Perez, M.A.; , "Multilevel Converters: An Enabling Technology for High-Power Applications," Proceedings of the IEEE , vol.97, no.11, pp.1786-1817, Nov. 2009.
- [5] Malinowski, M.; Gopakumar, K.; Rodriguez, J.; Pérez, M.A. "A Survey on Cascaded Multilevel Inverters," Industrial Electronics, IEEE Transactions on, vol.57, no.7, pp.2197-2206, July 2010.
- [6] Panagis, P.; Stergiopoulos, F.; Marabeas, P.; Manias, S. "Comparison of state of the art multilevel inverters," Power Electronics Specialists Conference, 2008. PESC 2008. IEEE, vol., no., pp.4296-4301, 15-19 June 2008.
- [7] Tolbert, L.M.; Fang ZhengPeng; Habetler, T.G. "Multilevel converters for large electric drives," Industry Applications, IEEE Transactions on, vol.35, no.1, pp.36-44, Jan/Feb 1999.
- [8] Fang ZhengPeng; , "A generalized multilevel inverter topology with self voltage balancing," Industry Applications, IEEE Transactions on , vol.37, no.2, pp.611-618, Mar/Apr 2001.
- [9] Teodorescu, R.; Blaabjerg, F.; Pedersen, J.K.; Cengelci, E.; Enjeti, P.N.; , "Multileve cascading industrial VSI," Industrial Electronics, IEEE Transactions on , vol.49, no.4, Aug 2002.
- [10] Nabae, Akira; Takahashi, Isao; Akagi, Hirofumi; , "A New Neutral-Point-Clamped PWM Inverter," Industry Applications, IEEE Transactions on , vol.IA-17, no.5, pp.518-523, Sept. 1981
- [11] T. A. Meynard and H. Foch, "Multi-level choppers for high voltage applications," in Proc. Eur. Conf. Power Electron. Appl., 1992, vol. 2, pp. 45–50.
- [12] M. Marchesoni, M. Mazzucchelli, and S. Tenconi, "A non-conventional power converter for plasma stabilization," in Proc. Power Electron. Spec. Conf., 1988, pp. 122–129.
- [13] De, S.; Banerjee, D.; Siva Kumar, K.; Gopakumar, K.; Ramchand, R.; Patel, C.; , "Multilevel inverters for low-power application," Power Electronics, IET , vol.4, no.4, pp.384-392, April 2011.