CONTROL SCHEME BASED ON Z-SOURCE FOR FOUR SWITCH THREE PHASE BRUSHLESS DC MOTOR

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Abstract:

This paper proposes novel four-switch three phase brushless dc motor control scheme based on quasi Z-source network which improves the utility ratio of DC voltage, increases the range of speed and minimizes the output torque ripple. The additionally partial circuits of quasi Z-source network and the bridge arm of four-switch inverter involves the quasi Z-source converter. During the operation of motor, shoot through states are applied. Then the input voltage of inverter extends and the performance of motor can be markedly improved. The dynamic performance of current is influenced and the speed is limited, for the irregular dc-link voltage utilization in the control of four-switch three phase Brushless DC motor. This paper presents a novel topology, which merges quasi Z-source converter and four-switch three phase drive circuit. The drawbacks of four-switch three-phase Brush less DC motor are analyzed; the control methods of each mode in quasi Z-source four-switch three phase topology are illustrated. Simulations for quasi Z-source four-switch three-phase Brushless DC motor were constructed in MATLAB/Simulink circumstance. To reduce the ripple in torque and distortion in current the quasi Z-source four-switch three-phase Brushless DC motor control scheme is proposed.

KEYWORDS*: Z-Source Network, BLDC Motor, DC link Voltage, Back EMF*

Introduction:

Brushless dc motor is mostly applied in various fields, because of its high power density, large output torque and quickly dynamic response, etc. Four-switch three-phase brushless dc motor is developed based on the driving circuit that is composed of conventional six-switch inverter. It has the merits of low-driven cost and less switching loss. Therefore, it is of great significance to research on performance enhancements of four-switch three-phase brushless dc motor.

According to the deficiency of FSTP brushless dc motor, many scholars at home or abroad put forward a series of improved strategy. In full dc-link voltage period, the distortion of phase current will happen for existence of C phase back-EMF. Consequently, the current control based voltage vector is adopted in paper [1-2]. It can make the C phase current converge to zero through inserting adjusting vectors. The method is easy to implement and has merits of fixed frequency, high stability and rapid dynamic response. In paper [3], the double closed-loop control that contains speed and current hysteresis is restrained effectively. To further reduce controlling costs, a novel control scheme of four-switch three-phase brushless dc motor without current sensor is presented in paper [4-5]. The commutation time of motor can be determined through the zero-crossing detection for terminal voltage. Meanwhile, the phase error is significantly decreased for no need of delaying 30 or 90 electrical angles. It combines four-switch three-phase inverter with the boost circuit to increase the input voltage of inverter by three effective- vector current control. Furthermore, it features the compact structure and simple arithmetic. We can provide an alternate strategy to control BLDC motor by using Four Switch Three Phase inverter. FSTPI helps reduction in switching losses, gate

54

drive requirements, circuit cost and computational time. At low power application, FSTPI fed BLDC motor drives are more suitable than SSTPI (Six Switch Three Phase Inverter). Also reconfiguration of these three leg inverter to FSTPI, in case of switch or leg failure can be done. In proposed system losses are minimizing due to FSTPI because there is reduction in switching losses and also due to Quasi Z-source Inverter output voltage is boosting hence output results were better.

A. Quasi Z-Source

A network that consists of a split-inductor and capacitors are connected in X shape is employed to provide an impedance source (Z-source) coupling the inverter. The dc source/or load can be either a voltage or a current source/or load. Therefore, the dc source can be a battery, diode rectifier, thyristor converter, fuel cell, an inductor, a capacitor, or a combination of those. The inductance can be provided through a split inductor or two separate inductors.

 B. BLDC Motor

The BLDC motor is an AC synchronous motor with permanent magnets on the rotor (moving part) and windings on the stator (fixed part). Permanent magnets create the rotor flux and the energized stator windings create electromagnet poles. The rotor (equivalent to a bar magnet) is attracted by the energized stator phase. By using the appropriate sequence to supply the stator phases, a rotating field on the stator is created and maintained. This action of the rotor, chasing after the electromagnet poles on the stator, is the fundamental action used in synchronous permanent magnet motors. The lead between the rotor and the rotating field must be controlled to produce torque and this synchronization implies knowledge of the rotor position. Conventional dc motors have many attractive properties such as high efficiency and linear torque-speed characteristics. The control of dc motors is also simple and does not require complex hardware however main drawback of the dc motor is to need periodic maintenance. The brushes of the mechanical commutator have other undesirable effects such as sparks. Despite the name, BLDC motors are actually a type of permanent magnet synchronous motors. They are driven by DC voltage.

- High efficiency
- Better speed versus torque characteristics
- Noiseless operation
- Higher speed range

METHODOLOGY

A. Introduction

In Proposed system, we are using Z-Source inverter and hence due to that output voltage is boosting and will become equal or greater than input voltage. Applied input voltage is same as applied to existing system also the PI controller is used in this proposed system to control the speed and torque ripple. The Hall sensor which works on Hall Effect gives signal to PWM generator and it generates pulses which are used to turn ON and OFF switches. Also as explained in Quasi Z-source network shown in fig. 1, there are two major working modes which are Shoot Through mode and Non-shoot through mode. In shoot through mode supply is cutting off and fifth switch is working as auxiliary switch which supply the voltage to inverter by discharging capacitor.

Fig. 1: Circuit topology of FSTP brushless drive

Only half of DC bus voltage participate in work on account of FSTP inverter notwithstanding, brushless DC motor operates under lower than rated voltage. The Quasi Z-Source network works in boosting status in mode I&VI. Then DC voltage of FSTP inverter increases and normal operation of motor would not be disturbed. At the moment, the power Udc and capacitor C3 recharge inductance L1 and L1 stored energy. Also capacitor C4 recharges inductance L2 and L2 stored energy.

The BLDC motor is fed by the power of capacitor C2. The reverse bias voltage across diode D1 blocks itself from working. Q5 is used because of preventing the capacitor C1 & C2 from being short circuited. In mode II & IV there exist current distribution in C phase back-EMF is not zero .There is relationship among A, B, C, phase current would happen if the switches Q1& Q4 are gated by control signal. To solve this problem, two switch of one bridge arm should be controlled independent theoretically, the

Fig. 5. Implementation scheme of a DTC strategy of FSTPI-fed BLDC motor drives inspired from the one considering the case where the BLDC motor is fed by a TWTPI-inverter.

56

Simulation Results:

To evaluate the performance of the proposed PMBLDCM drive system, simulation models have been developed and the simulation is carried out using MATLAB/ SIMULINK. Fig. 6 shows MATLAB/SIMULINK model of closed loop speed control of PMBLDCM drive using FSTPI. The performance of the drive is simulated for constant rated torque (10 Nm) at rated speed.
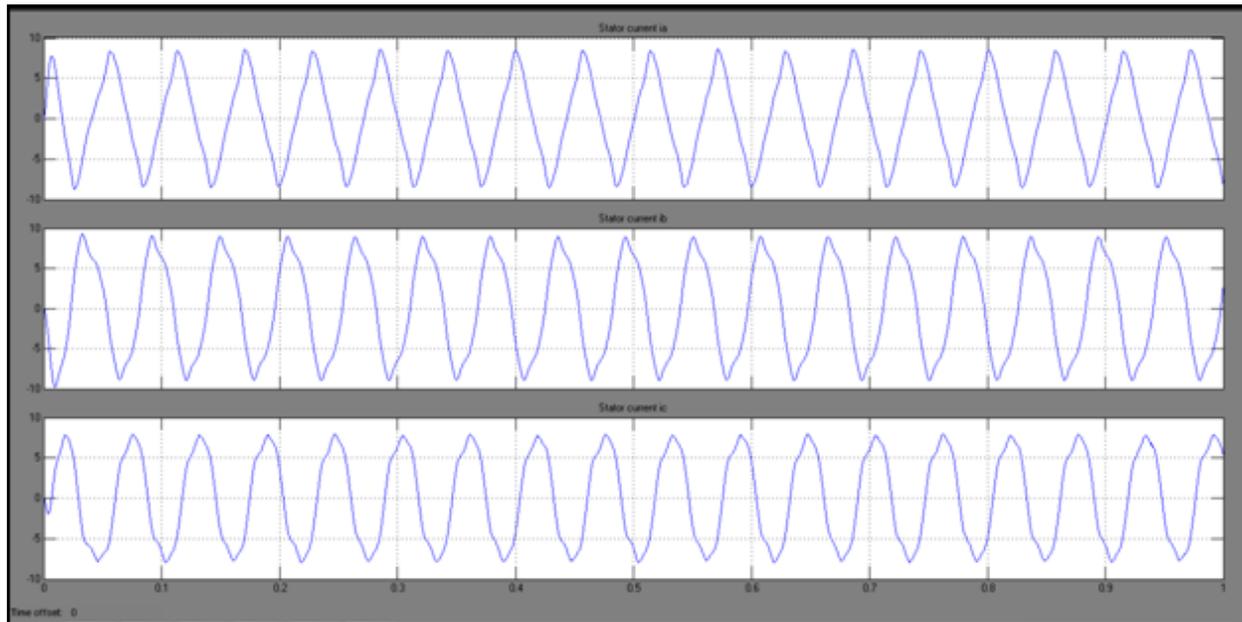
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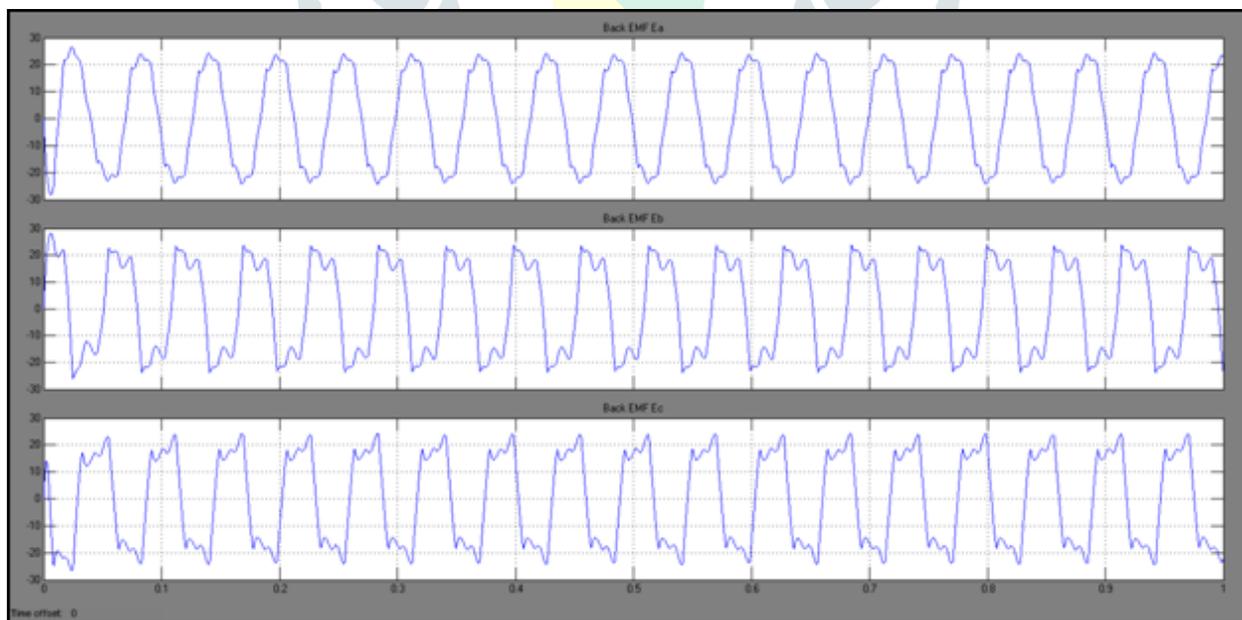
Back emf waveforms of the three phases.

CONCLUSION

The presented paper is an outline of the worked novel four-switch three-phase brushless dc motor control scheme based on quasi Z-source network, which combines FSTP BLDC motor and quasi Z-source network. The input voltage of FSTP inverter will be boosted to enlarge the range of speed and will be enhance the ability with load when quasi Z-source converter works. The quasi Z-source network is employed to boost the DC voltage, and the problem that the speed of motor restricted to solved under the condition of low DC voltage. The QZFSTP motor circuit is simulated; the new topology having some advantages such as boosting DC voltage, rapid response and extending the range.

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