



Journal of Emerging Technologies and Innovative Research

An International Open Access Journal Peer-reviewed, Refereed Journal

www.jetir.org | editor@jetir.org An International Scholarly Indexed Journal

Certificate of Publication

The Board of

Journal of Emerging Technologies and Innovative Research (ISSN : 2349-5162)

Is hereby awarding this certificate to

A.RAJESH

In recognition of the publication of the paper entitled

Dual-VDD, Single-Frequency Clocking Methodology for System on Chip

Published In JETIR (www.jetir.org) ISSN UGC Approved (Journal No: 63975) & 7.95 Impact Factor

Published in Volume 2 Issue 6 , July-2015 | Date of Publication: 2015-07-03

Parisa P

EDITOR

[Signature]

EDITOR IN CHIEF

JETIR1506068

Research Paper Weblink <http://www.jetir.org/view?paper=JETIR1506068>

Registration ID : 150610





Journal of Emerging Technologies and Innovative Research

An International Open Access Journal Peer-reviewed, Refereed Journal

www.jetir.org | editor@jetir.org An International Scholarly Indexed Journal

Certificate of Publication

The Board of

Journal of Emerging Technologies and Innovative Research (ISSN : 2349-5162)

Is hereby awarding this certificate to

Dr.B.L.Raju

In recognition of the publication of the paper entitled

Dual-VDD,Single-Frequency Clocking Methodology for System on Chip

Published In JETIR (www.jetir.org) ISSN UGC Approved (Journal No: 63975) & 7.95 Impact Factor

Published in Volume 2 Issue 6 , July-2015 | Date of Publication: 2015-07-03

Parisa P

EDITOR

EDITOR IN CHIEF

JETIR1506068

Research Paper Weblink <http://www.jetir.org/view?paper=JETIR1506068>

Registration ID : 150610





Journal of Emerging Technologies and Innovative Research

An International Open Access Journal Peer-reviewed, Refereed Journal

www.jetir.org | editor@jetir.org An International Scholarly Indexed Journal

Certificate of Publication

The Board of

Journal of Emerging Technologies and Innovative Research (ISSN : 2349-5162)

Is hereby awarding this certificate to

Dr.K.Chenna Kesava Reddy

In recognition of the publication of the paper entitled

Dual-VDD,Single-Frequency Clocking Methodology for System on Chip

Published In JETIR (www.jetir.org) ISSN UGC Approved (Journal No: 63975) & 7.95 Impact Factor

Published in Volume 2 Issue 6 , July-2015 | Date of Publication: 2015-07-03

Parisa P

EDITOR

EDITOR IN CHIEF

JETIR1506068

Research Paper Weblink <http://www.jetir.org/view?paper=JETIR1506068>

Registration ID : 150610

