



Standard for Low-Density Parity-Check Codes and Advanced Cryptographic Authentication in 5G systems

¹ Rahul Sharma, ² Aakanksha S. Choubey,

¹ M.Tech Research Scholar, ² Professor

^{1,2} Computer Science Engineering

^{1,2} Shri Shankarcharya Technical Campus Bhilai, India

Abstract: Digital communications have always faced significant dependability and security challenges. Latency reduction is one of the most crucial new needs brought forth by the developing 5G technologies. A combined security and reliability solution is put forth in this research to meet both objectives in a single step with lessened computing complexity, which lowers latency for 5G communication systems. The suggested technique simultaneously performs encryption and encoding using the Low-Density Parity Check (LDPC) codes and the Advanced Encryption Standard (AES) cryptosystem. The suggested technique outperforms earlier traditional methods that execute encoding after encryption, according to MATLAB simulation findings. From 2G to 4G, digital communications have seen many developments in recent years. The rapidly expanding needs for communication technology created new expectations that the next 5G network must meet. Reduced latency is one of several modifications that these new applications, however, need. The propagation and switching delays, as well as the computational complexity of the systems, are what generate latency.

IndexTerms - DES, low-density parity-check (LDPC) code, AES-LDPC Encryption/Encoding, Key Generation and Expansion.

1. INTRODUCTION

Since the introduction of 2G and 4G digital communications, several developments have taken place. New needs were brought about by the rapidly expanding demands for communication technologies [1]. Three scenarios—ultra-reliable and low-latency communication (URLLC) enhanced mobile broadband (eMBB) and massive machine-type communication (mMTC)—are at the heart of 5G. These three scenarios enable a wide range of applications, including the internet of things (IoT), traffic control and protection, critical industrial and infrastructure applications, and very high-speed data delivery up to 1Gbit/s. The science of encrypting and decrypting data using mathematics is known as cryptography. With the help of cryptography, you may store and send private data in a way that only the intended receiver can read it over public networks like the Internet. to safeguard the data's security.

2. LITERATURE REVIEW

Encryption is used to protect data from being accessed by unauthorized parties. The Data Encryption Standard (DES), a block cypher encryption method, was intended to be replaced with AES in the early 2000s for improved data security. Its "plaintext" input data, which is a fixed length of 16 bytes (128 bits), accepts various encryption keys with lengths of 128 bits, 192 bits, and 256 bits. A key and a non-linear mapping technique known as the S-Box are used to transform the plaintext into cipher text. The plaintext once the key has been added, In each Nr-1 round, the encryption process performs four transformations, including Sub Byte, Shift Row, Mix Column, and Add Round Key, and cancels the Mix Column block in the last round Nr. The initial encryption key is split into sub keys corresponding to each round for AES-128 bits, where $Nr = 10$ (12 for AES-192 and 14 for AES-256) and $Nr = 10$ (10 for AES-192 and 14 for AES-256). In the algorithm, sub Bytes handle substitution, while Shift Rows and Mix Columns handle permutation. This action carries out the replacement. Each byte is replaced with another byte in this phase. Sub Bytes is a byte substitution procedure that generates new values by first passing each byte through a separate function. The byte is then transformed into another value by utilizing its hexadecimal code using a table known as the S-box. The nonlinear byte substitution table (S-box) used by the Transformation in the Cypher to process the State acts on each of the State bytes separately. A linear error correcting code, or low-density parity-check (LDPC) code, is a way to send a message across a noisy transmission channel. A sparse Tanner graph (subclass of the bipartite graph) is used to build an LDPC. Applications seeking highly reliable information transfer across bandwidth- or return-channel-constrained networks while contending with erroneous noise are increasingly turning to LDPC codes. LDPC code implementation has lagged behind those of other codes, particularly turbo codes. Gallager made the original discovery of LDPC codes in the early 1960s. In contrast to its earlier codes, such as Turbo codes, LDPC codes later had a significant rediscovery in the late 1990s, during which a significant quantity of literature is being presented to

attain greater performance near to the Shannon limit. The National Institute of Standards and Technology (NIST) has released the Data Encryption Standard (DES), a symmetric-key block cypher. A Feistel Cypher is implemented in DES. 16 round Feistel structures are used. Blocks are 64 bits in size. Even though the key length is 64 bits, DES actually uses 56 bits as its actual key length since 8 of the key's 64 bits are only used as check bits. The following graphic shows the general structure of DES. Since DES is based on the Feistel Cypher all that is needed to define it is the round function, key schedule, and initial and final permutation.

3. METHODOLOGY

The joint encryption and error correction techniques outlined in the literature mentioned above struggle to reduce the computing complexity of the system while retaining excellent security and error correction performance. However, the suggested approach may provide high security and strong error-correcting capabilities with minimal complexity, making it appropriate for future communication systems like 5G that demand low latency.

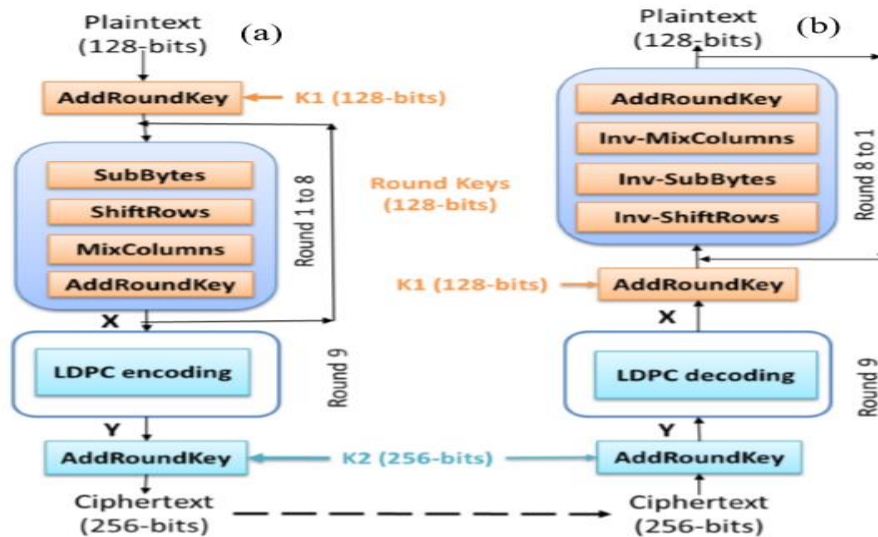


Figure 3.1: The proposed AES-LDPC system's structure

i. AES-LDPC Encryption/Encoding

The goal of this approach is to combine LDPC encoding with AES encryption in order to simplify computations and enhance system performance. The suggested combined AES-LDPC in this article combines AES operations with the LDPC code to encrypt the plaintext as shown in Figure, achieving all the aforementioned properties.

ii. AES-LDPC Decoding/Decryption

The decryption procedure, which may be seen in Fig., is stated as follows and is the reverse of the encryption procedure:

- Ciphertext=Data (256 bits)
- $K2 \oplus Y = \text{Ciphertext}$
- $X = \text{LDPC decoding}(Y)$
- $X \oplus K1 - 8 \text{ 1} = \text{Plaintext AES} (,)$

iii. Key Control

Two encryption keys, such as K1 with a length of 128 bits and K2 with a length of 256 bits, are employed in the suggested joint system. Following the LDPC encoding, K2 is used to expand K1 using the usual AES key, w35) as expansion technique for 8 rounds (w0, w1, illustrated in Fig. 8). Since the LDPC codes and the AES algorithm are both openly available, the proposed method's security also relies on the confidentiality of the encryption keys. As a result, K1 and K2 are crucial to the system's security and must be carefully managed by authorized users. In contrast to the conventional AES technique, the suggested solution boosts security by using two encryption keys. Even if an attacker obtains just one encryption key, the decryption procedure, which may be seen in Fig., is stated as follows and is the reverse of the encryption procedure:

Because just one encryption key is utilized in the typical AES technique, the suggested approach has a greater security level.

iv. Performance Analysis

The performance of security against cryptanalysis attacks, the efficiency of error correction, and the processing time that define computational complexity are used to demonstrate the gain of the proposed combined AESLDPC technique. The links between encryption and encoding in the overall performances of the system are also demonstrated using numerical figures.

v. Processing Time

Table I, where TX stands for the Transmitter side and RX for the Receiver side, compares the processing times of the proposed technique to the standard method. The AES algorithm runs 8 rounds in the joint method rather than 10 rounds, which provides a reduction in complexity of 2 rounds on both sides. The encoder without code block size calculation (bs Tldpc) receives the encrypted data as input in the ninth round, which uses the LDPC code. In the decoding decryption (bs Taes) portion, the opposite procedure is carried out. When large amounts of data are transferred, more plaintext M are generated and more processing time is gained. Only the tenth round of AES is replaced by the LDPC code in the literature [24], resulting in a twofold slower processing time gain than our approach.

Table 3.1: Processing Time And Reduction Gain

Methods	Side	Processing time	Reduced Gain
Conventional method	TX	$T_{TX} = T_{as}^{bs} + M \times T_{as}^{cs} (10 \text{ rounds}) + T_{lpc}^{bs} + N \times T_{lpc}^{cs}$	0
	RX	$T_{RX} = T_{lpc}^{bs} + N \times T_{lpc}^{dc} + T_{as}^{bs} + M \times T_{as}^{dc} (10 \text{ rounds})$	0
Proposed AES-LDPC method	TX	$T_{TX} = T_{as}^{bs} + M \times T_{as-lpc}^{cs} (9 \text{ rounds})$	$M \times T_{as}^{cs} (2 \text{ rounds}) + T_{lpc}^{bs}$
	RX	$T_{RX} = T_{lpc}^{bs} + M \times T_{as-lpc}^{dc} (9 \text{ rounds})$	$T_{as}^{bs} + M \times T_{as}^{dc} (2 \text{ rounds})$

vi. Security Performance

The suggested AES-LDPC method's level of security is evaluated by looking at how well-known cryptanalysis methods may break it.

a) Resistance to differential attack

The attempt to determine the encryption key by following the rounds and examining the difference propagation property of cypher pairs is also known as a chosen-plaintext attack [33]. The threshold probability to achieve differential uniformity on a non-linear function of n bits is $(1/2^n) \cdot 2^{Pd}$, which has the same cryptanalysis difficulty as an $O(2^n)$ brute force assault. Two distinct keys, each with a size of $n=128$ and $n=256$ bits, are utilized to secure the system in the proposed AES-LDPC system. To obtain both $K1$ and $K2$, an attacker would need to generate $2^{128} = 3.4 \times 10^{38}$ possibilities, which is still not feasible given today's processing technology ($2^{56} = 1.1 \times 10^{17}$, as previously demonstrated).

b) Resistance to saturation or square attack

This uses balance changes rather than an exhaustive search in each round of the AES algorithm to try to estimate the key bytes in the fourth round [32]. Previous research, as mentioned in [24], has demonstrated that higher rounds of AES are safe from this attack on 6, 10, 18, and rounds with a complexity of $2^{63} = 9.22 \times 10^{18}$. Because the proposed system employs 8 rounds of AES encryption, it is protected against saturation attacks. Furthermore, the AES method does not utilize the second key, $K2$, making it safe against saturation attacks. Additionally, the natural diffusion of the LDPC code d_{min} with one of the AES 8 rounds is added by the suggested AESLDPC's diffusion complexity. The 256-bit LDPC code's minimum distance is $d_{min} = 16 = 42$. The diffusion complexity is 49 for the conventional AES-128 (10 rounds) and drops to 47 when 2 rounds are skipped. Therefore, our method's total diffusion is $47 + 4 = 49$, maintaining the normal AES128's diffusion complexity. As a result, this technique may withstand a saturation assault in a manner similar to AES with 10 rounds.

vii. Resistance to power analysis attack

In this assault, the perpetrator uses the necessary tools to measure the amount of information that has been compromised throughout the encryption process. Following a statistical examination of power curves, he conjectures the key to unlock the data. The output phase of an AES encryption cycle is where power analysis attacks frequently decide to execute. Based on this, any AES technique utilising a single key, such as in and, may be weak as it may be attacked at the earliest stage, making it simpler to obtain the key. The suggested method is still secure even if an attacker manages to obtain the first AES round key using this way since a brute force assault is required to obtain the second key. A set of tests are run to numerically analyses the security robustness of the method using the following parameters in addition to the security performance analysis in terms of known attacks. Entropy is a measurement of disorder, disarray, and confusion in a picture. Entropy is used to gauge the degree of confusion in the picture data since the primary goal of encryption is to conceal the substance of data. A collection of photos from the database, as seen in Fig. 2, is utilized to verify proper encryption conditions. The results of the entropy calculation on the database's original photos and matching encrypted images are displayed in Table II. According to the findings, all entropy levels are near to the maximum confusion value of 8. As a result, the technique can provide strong protection from entropy analysis.

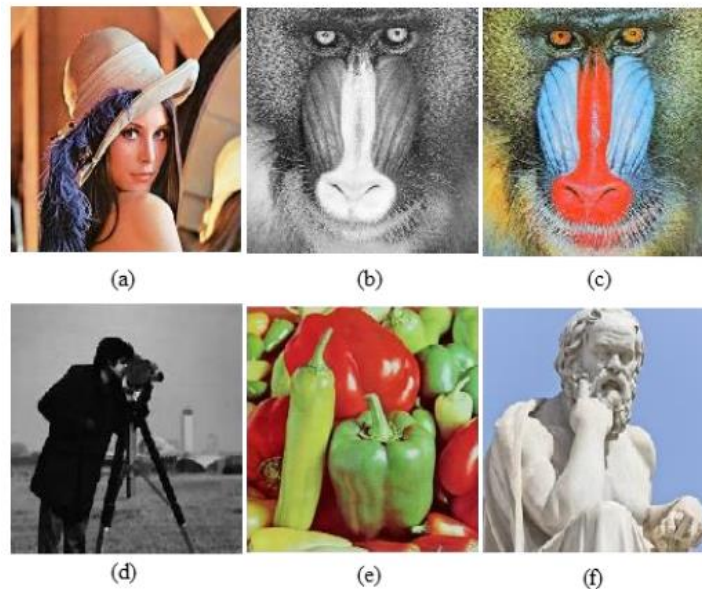


Figure 3.2: Simulation images for entropy and correlation calculations

Table 3.2: Entropy and Correlation Values

Images	Entropy Original image	Entropy encrypted image	Correlation
Fig.11 (a)	7.9174	7.7228	1.8051e-08
Fig.11 (b)	7.6829	7.9386	-2.8546e-08
Fig.11 (c)	7.7616	7.7245	1.2442e-08
Fig.11 (d)	7.0002	7.7198	1.2886e-07
Fig.11 (e)	7.7474	7.7194	-3.7946e-08
Fig.11 (f)	7.1974	7.7203	-7.7642e-08

An example of a histogram that visualizes the tonal distribution in a digital image is an image histogram. Each tonal value's number of pixels is plotted. A viewer will be able to quickly assess the complete tonal distribution of a picture by taking a quick look at the histogram for that particular image. The distribution of pixels in a picture according to their brightness is shown by the histogram of that image.

4. RESULTS AND DISCUSSION

To test the performances of encryption and error correction, simulations are conducted in MATLAB with the parameters in Table

Table 4.1: Simulation Parameters

PARAMETER	VALUE
Block length	128-bits
Key length	128 and 256-bits
Code length	256-bits
Code rate	1/2
Modulation	BPSK
Channel	AWGN
Decoding algorithm	Sum-Product
Number of Iteration	5

Since the AES-LDPC encoder's coding rate is $R=1/2$, the input data must be organized in blocks of 128 bits in order to be encrypted with the first key (K1) and produce 256-bit encoded data. Next, the second 256-bit key (K2) is inserted to the 256-bit block (XOR). The data then enters the Additive White Gaussian Noise (AWGN) channel and the Binary Phase Shift Keying (BPSK) modulator, before being sent. After that, the inverse method is used to retrieve the obtained 256-bit data. The following diagram illustrates the simulation outcomes for the combined AES-LDPC approach. The input data for the simulation is the original picture in Fig. 2. As seen in Fig., it is encrypted using the AES industry standard Fig. 3 (a), which was then encrypted and encoded using the suggested AES-LDPC approach. The received picture is recovered and analyzed in accordance with the

channel's signal-to-noise ratio (SNR) parameters after transmission across the AWGN channel. When the SNR is 0dB (Fig. 14), the error diffusion effect has completely changed the signal and prevents the image from being recovered; when the SNR is 0.5dB (Fig. 5), the image can hardly be recovered; at 1dB (Fig. 4), users can essentially recognize the image; when the SNR is 1.5dB, the image can be seen by users but still contains some noise; and when the SNR is 2dB (Fig. 5), the image can be seen through which the picture is obtained. The error-correcting performance findings in Fig., however, demonstrate that when SNR is 2.5dB, the image is fully recovered with no mistake. When SNR is equal to or greater than 2dB, the original picture may be seen well with varying SNR values ranging from 0dB to 2.5dB.

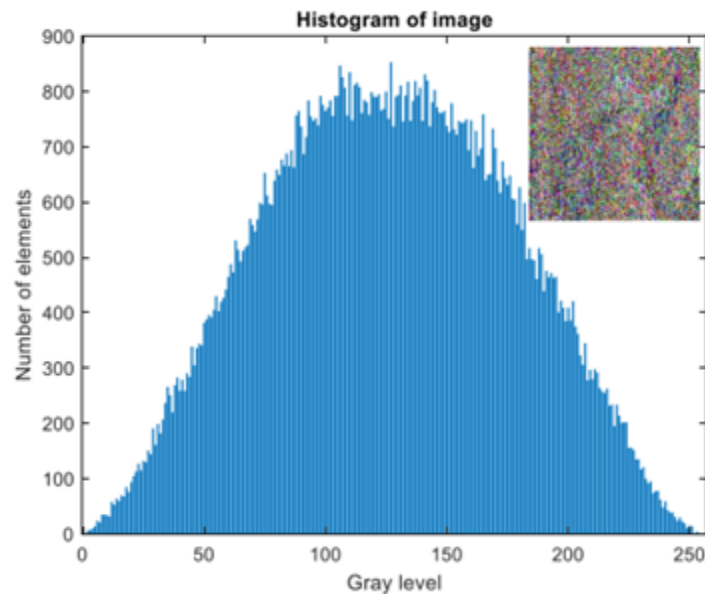


Figure 4.1: Recovered Image at (SNR=0dB)

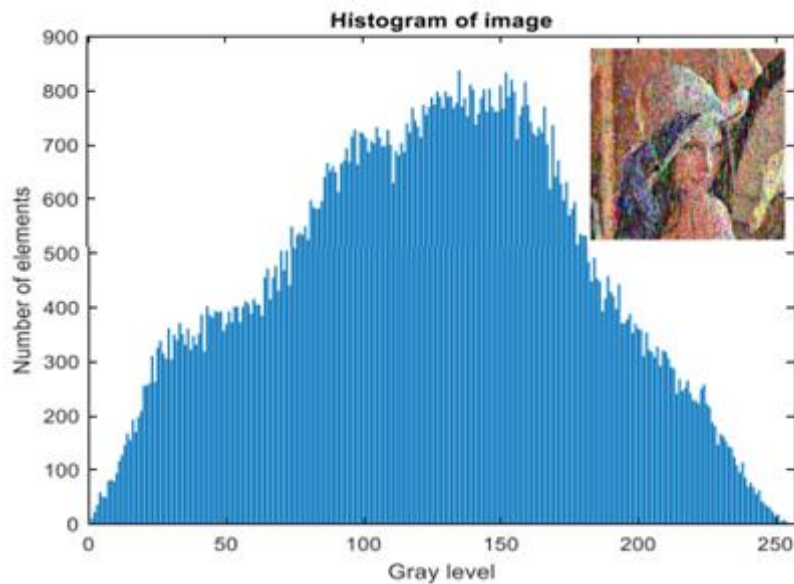


Figure 4.2: Recovered Image at (SNR=0.5dB)

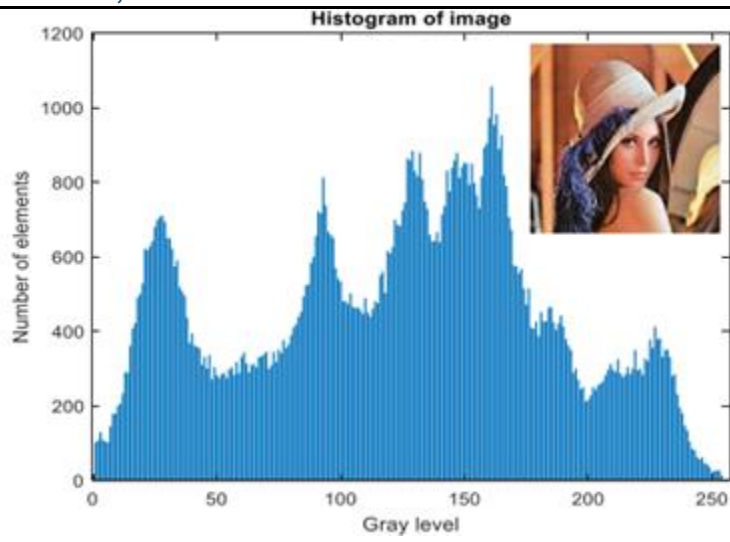


Figure 4.3: Recovered Image at (SNR=2dB)

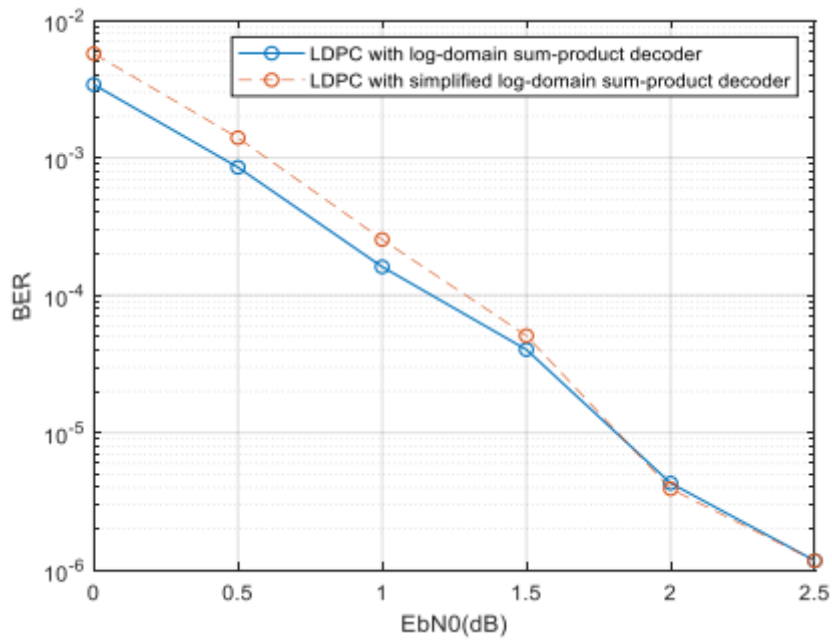


Figure 4.4: The error correcting code

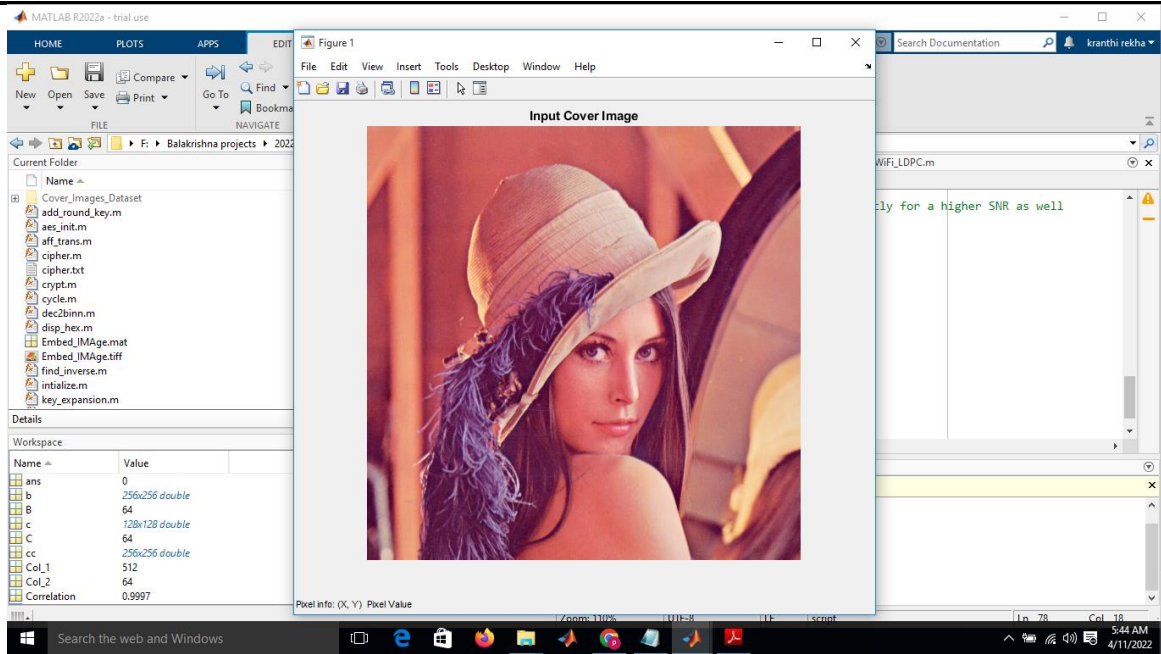


Figure 4.5: Input Covered Image

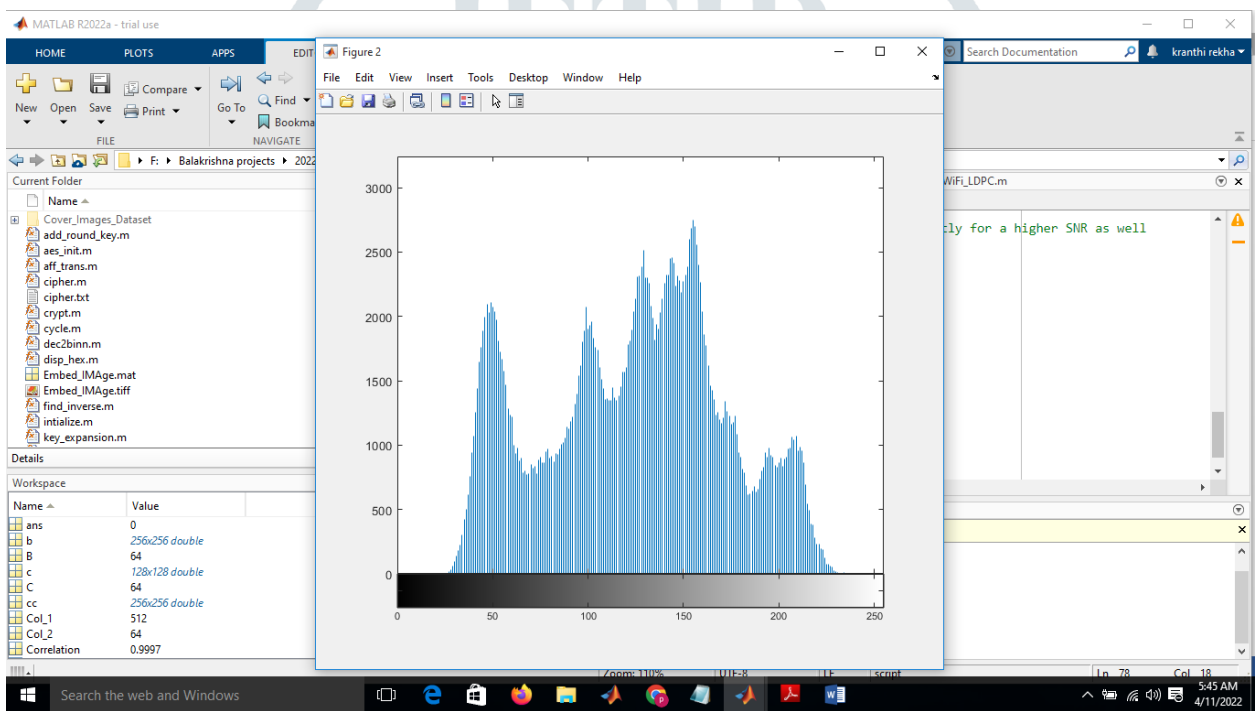


Figure 4.6: Histogram View of Input images

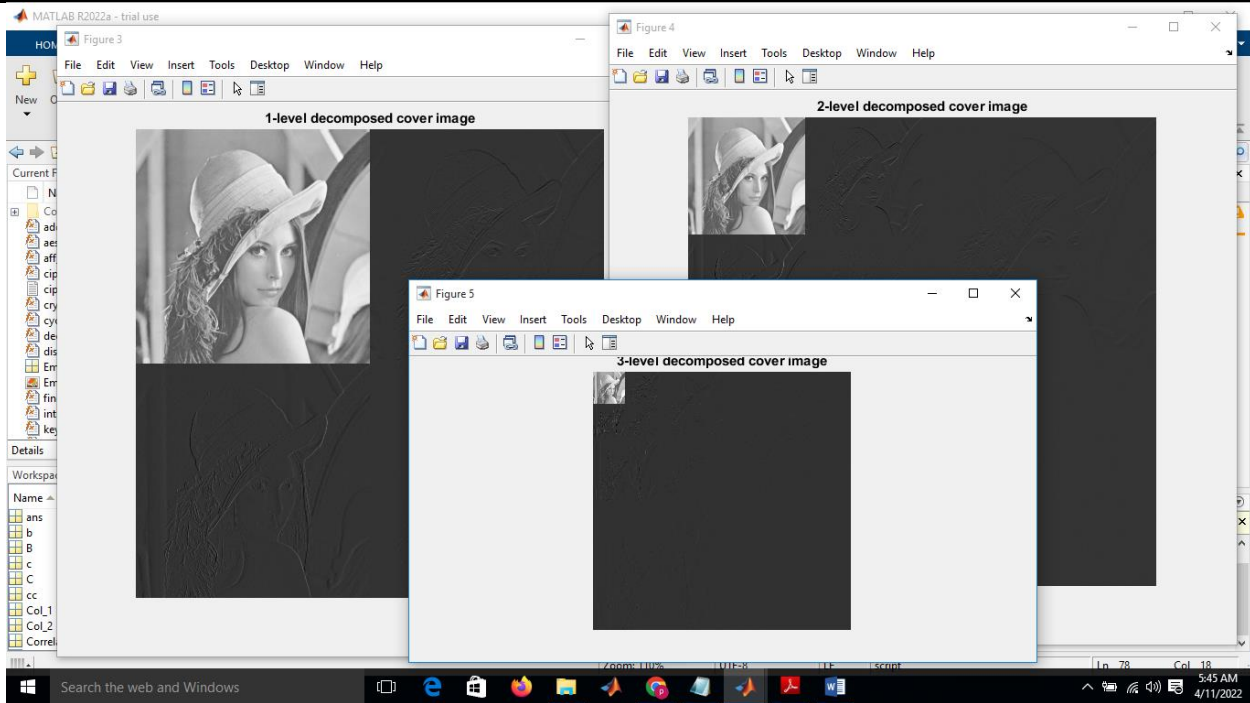


Figure 4.7: 3 Level Decompose Images

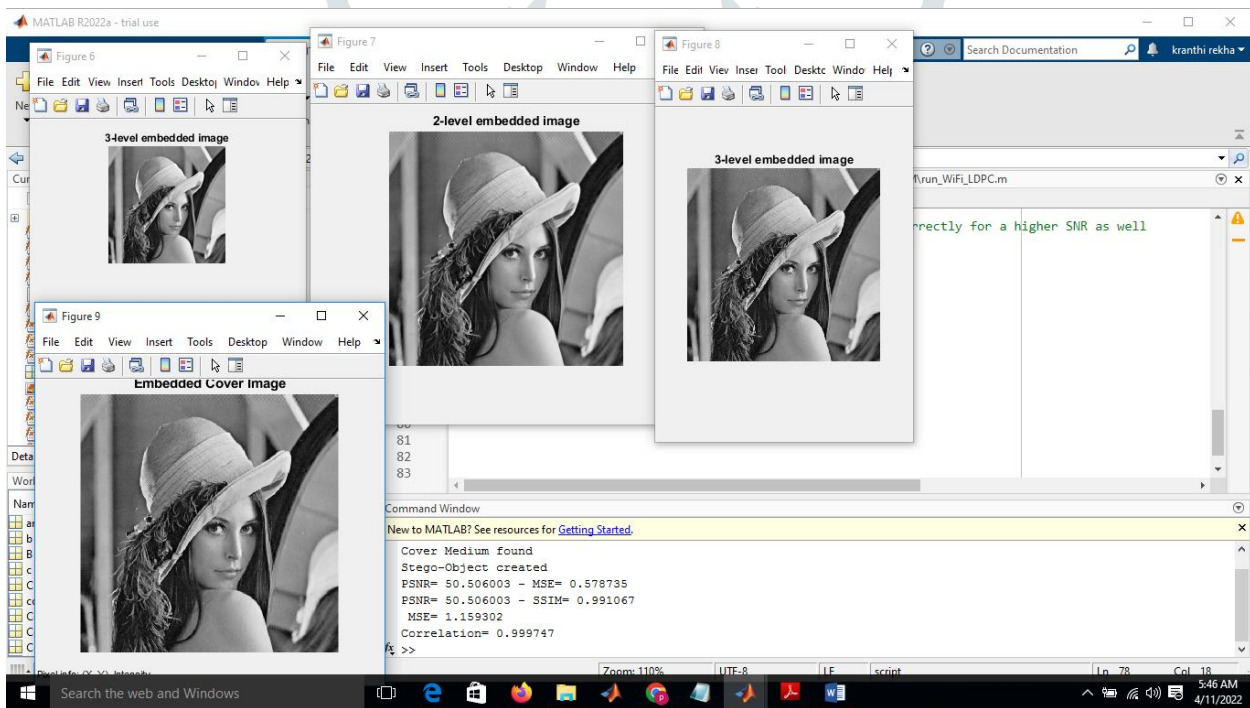


Figure 4.8: 3 level embedded Images

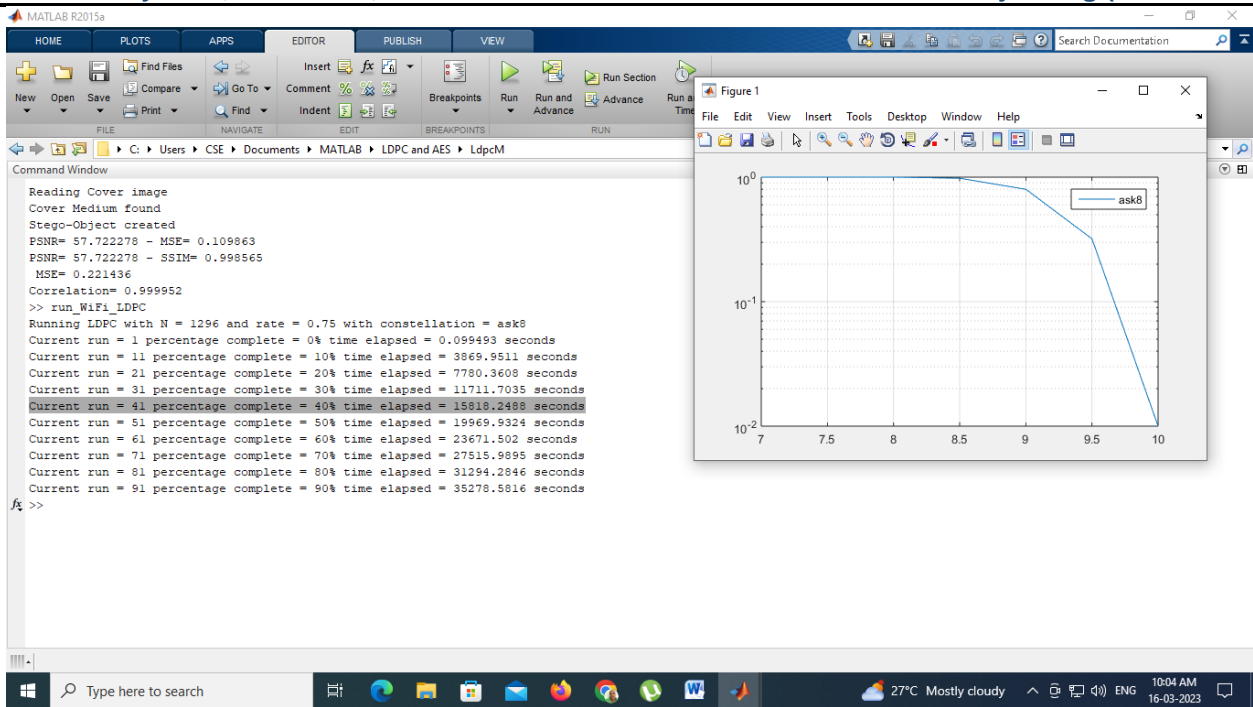
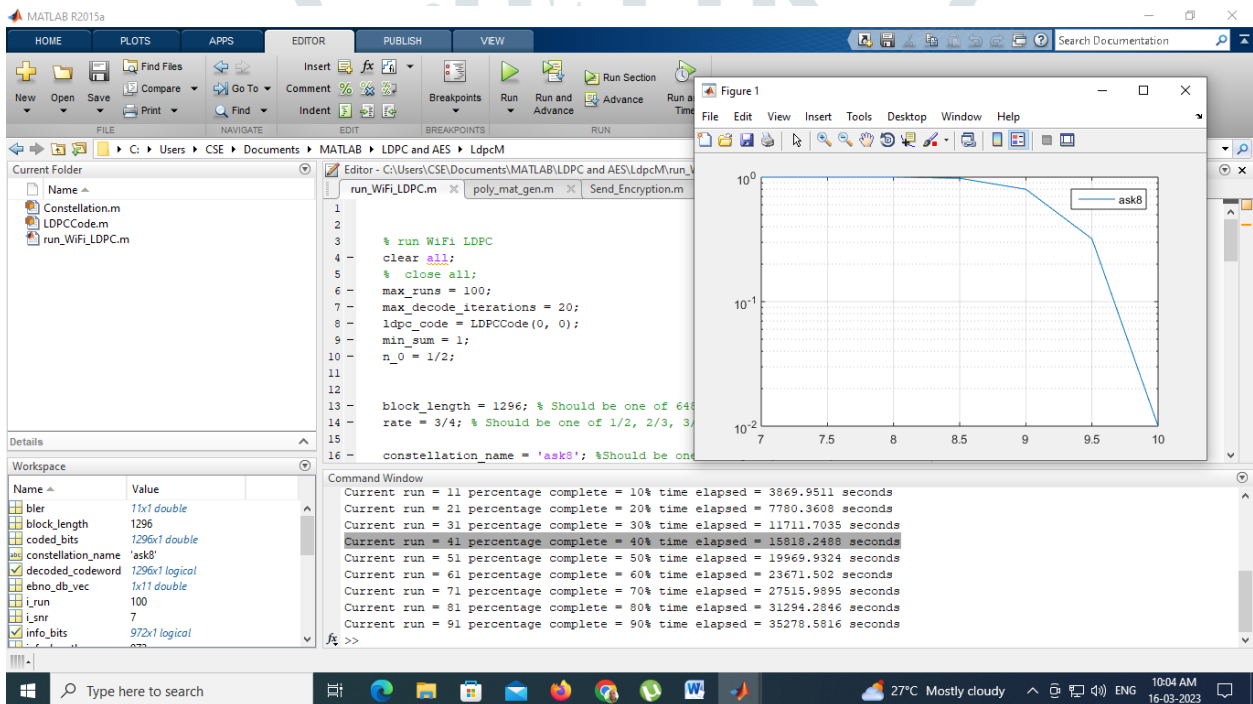


Figure 4.9: MSE output of LDPC



can accurately recover image data at SNR equal to or greater than 2dB. The error correction capability of the LDPC code is maintained in the proposed method while its performances are used to improve the security level of the AES algorithm. Due to the removed two rounds of the AES algorithm and the code block size determination cancelled in the LDPC code and their parallel activation, a high processing time gain is achieved as compared to the conventional methods. The joint method of AES and LDPC perform well with reduced computational complexity without compromising the performance of each other.

ACKNOWLEDGEMENTS

I am very thankful to my guide and friend who help in research work in this section.

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