MULTI OBJECTIVE BASED ANALYTICAL APPROACH FOR OPTIMAL PLACEMENT OF DISTRIBUTED GENERATORS IN POWER SYSTEM

Sanjeeva Kumar R A, H R Sudarshan Reddy, Anantapadmanabha T

1 Research scholar, UBDT, Davangere,
2 Professor, UBDT, Davangere,
3 Principal, NIEIT, Mysuru,

Abstract: With the increase in demand of electrical power, distributed generation plays a vital role in fulfilling the requirement of good power quality to be supplied to the consumer without interruption. Placement of distributed generator (DG) is important point to be considered in the distribution system as the location and size of DGs will have both positive and negative impact on the power system as improper placement may lead to increase in power loss and variation in voltage profiles, hence the optimal allocation is considered in the power system to reduce the power losses as well as to improve the voltage profiles. In this paper optimal location of DG is selected based on analytical method called Logarithmic voltage deviation index (LVDI) with different levels of DG penetration for TYPE I, TYPE II, and TYPE III DGs and active power losses are calculated after load flow and performance of voltage profiles are analyzed. Analysis is carried out on standard IEEE 33bus test system using Matlab.

Key words: Distributed generation, DG penetration, Logarithmic voltage deviation index, optimal allocation, power losses, voltage profiles,

1. Introduction
The term "distributed generation" (DG) refers to the production of electricity near the load center at the distribution level. The distributed generation resources are renewable energies and cogeneration (simultaneous production of heat and electricity). Distributed Energy Resources (DER) had changed many features of distribution system operation, design and Execution. With increase in number of decentralized systems with smaller generating units connecting directly to the distribution networks near load center, the distribution companies reduce loss in their networks. The position and capacity of the DG must be optimized to increase the benefits and minimize their negative impacts on the power system. The technical parameters are voltage profile, system protection, reactive power, power loss, power quality, power control and conditioning, reliability and stability. Improper placement of DG will lead to the increment in system losses which includes both active and reactive power losses, operating cost and network capital cost. It also leads to injection of harmonics, possibility of reverse power flow, risking the safety issues and complex controlling scheme. In view of these, proper allocation of distributed generation could enhance the network potential in terms of better voltage profile and lower the system power losses, while improving the quality of power supply. With the above considerations the optimal allocation and sizing of DG is necessary to improve the overall efficiency of the system by reducing the losses and improving voltage profiles and performance of the power system.

2. Problem formulation
Allocation of DG includes placement of DG at suitable position, with suitable capacity (total with the addition of multiple DG’s), and type of DG. There are four types of DGs available:

a) DG that injects active power only, for example photovoltaic
b) DG that injects reactive power, such as induction generators.

c) DG that injects active power, but absorbs reactive power, for instance induction generator
d) DG that injects both active and reactive power, for example synchronous generator

2.1. Power Loss Calculation
The power loss equation is given by

\[ P_{loss} = \sum_{k=1}^{n} G_k \left( |V_i|^2 + |V_j|^2 - 2 |V_i||V_j| \cos(\delta_i - \delta_j) \right) \]

The power loss equation is given by

\[ \text{minimize } P_{loss} = \sum_{i=1}^{n} (P_{loss} + Q_{loss}) \]
Voltage at the nodes should be within the limits

\[ V_{\text{min}} \leq |V_i| \leq V_{\text{max}} \]

Where \(|V_i|\) is the voltage magnitude at bus \(i\) and \(V_{\text{min}}\) and \(V_{\text{max}}\) are the minimum and maximum voltage limits respectively.

### 2.1.1. To find the optimal location of DG

“The location of DG is chosen as the one that gives the best voltage profile. This could be done by injecting the DG at each node calculating the voltage index by using equation. The best location is selected as the node with maximum logarithmic voltage deviation index (LVDI).”

\[ \text{LVDI} = \log \frac{V_{\text{new}}}{V_{\text{old}}} \]

Where \(V_{\text{new}}\) is voltage after dg placement and \(V_{\text{old}}\) is the voltage before the placement of dg. In this work, the DG are placed at the weak voltage nodes in ascending order and LVDI is calculated, the bus giving highest LVDI with the better voltage profile and lowest power loss is selected as the optimal location. “Hence, the algorithm consists of optimal sizing and sitting by using the proposed index (LVDI)”.

### 2.1.2. Sizing of DG

Penetration level of DG is chosen based on the following equation. Active power demand is considered for TYPE I DG installation and similarly reactive and apparent power demands are considered for TYPE II and TYPE III DGs.

\[ \text{PLDG\%} = \frac{\text{size of DG}}{\text{total demand}} \times 100 \]

The objectives function for the DG Placement for minimization of real power loss and LVDI of DG simultaneously. Here, the fitness function for the set objectives is formulated as given below:

\[ F = F_1 + F_2 \]

Where, \(F_1\) is the power loss minimization index and \(F_2\) is the voltage profile improvement by reducing the deviation index. The set of equality and inequality constraints are,

\[ Q_{Gi} - Q_{Di} = \sum_{j=1}^{NB} |V_i||V_j||Y_{ij}|\sin(\delta_i - \delta_j - \theta_{ij}).i = 1,2,\ldots,Ng \]

\[ P_{Gj}^{\text{min}} \leq P_{Gj} \leq P_{Gj}^{\text{max}} \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots j = 1,2,\ldots,Ng \]

\[ Q_{Gj}^{\text{max}} \leq Q_{Gj} \leq Q_{Gj}^{\text{max}} \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots j = 1,2,\ldots,Ng \]

\[ P_{DG}^{\text{min}} \leq P_{DG} \leq P_{DG}^{\text{max}} \]

\[ V_{n}^{\text{min}} \leq V_{n} \leq V_{n}^{\text{max}} \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots n = 1,2,\ldots,Nd \]

### 3. Proposed algorithm

In this paper, the DG are placed at the maximum power loss nodes in descending order and LVDI is calculated, the bus giving highest LVDI with the better voltage profile and lowest power loss is selected as the optimal location. “Hence, the algorithm consists of optimal sizing and sitting by using the proposed index (LVDI)”.
Where \( V_{\text{new}} \) is voltage after load flow and \( V_{\text{old}} \) is the base case voltage before load flow and \( \text{LVDI} \) is calculated, the bus giving highest \( \text{LVDI} \) is selected as initial bus for placement of DG. And load flow is run with integration of DG at different penetration levels and \( \text{LVDI} \) is calculated for base case voltage and new voltage obtained after load flow with DG integration, the bus giving highest \( \text{LVDI} \) with the better voltage profile and lowest power loss is selected as the optimal location. “Hence, the algorithm consists of optimal sitting by using the proposed index (LVDI) for different levels of DG penetration”.

4. Results and discussion

In this work, different types of DGs are discussed and are considered in the analysis and made to operate at different penetration levels in the power system and impact of those are studied which results in reduced power losses and improved voltage profiles and this is achieved by a new analytical technique called Logarithmic voltage deviation index.

The plots of voltage profile and power loss using type I, II and III DGs at 50% penetration are as shown. The comparison of the voltage profiles by introducing different types of DG’s is shown below.

![Comparison of voltage profiles at bus 8 for different type of DG’s](image-url)

The LVDI is carried out and calculated by considering three different methods to allocate DG for the initial case i.e, Minimum Voltage bus method, Maximum power loss method and a method considering initial base case voltage and the voltage after load flow as mentioned in the algorithm and flow chart. LVDI values of TYPE I, II and III DGs after 50% penetration.
The LVDI values obtained by integrating different TYPE of DGs are plotted in fig 3. And for the plot the node with maximum value is chosen as the best location where the best location obtained is at bus 6. The voltage profiles of different DGs at 50% penetration level can be observed in fig 4 and the graphs shows the variation of voltage at different nodes of different TYPES of DGs and variation of power losses at all nodes of IEEE33 bus test system.

![Fig 3: Comparison of LVDI values before and after installation of DG](image)

Voltage profiles of IEEE33 bus at best location obtained from LVDI values at bus 6 for base case and with DG penetration at 50% for TYPE I, II and III. The minimum voltage and power losses before and after placement of all three types of DGs at the node 8 and node 6 are tabulated in table 1 and 2 respectively.

![Fig4: Comparison of voltage profiles of bus 6 for different type of DG’s](image)

Power loss and voltage profile at best location bus 6, for 50% DG penetration TYPE I, II and III are tabulated below and it is ascertained that after the optimal allocation of DG at 6th bus there is an enhancement in the system performance.

### Table 1: Values of voltages and losses for initial case

<table>
<thead>
<tr>
<th>Cases</th>
<th>Initial case</th>
<th>VMin (Before)</th>
<th>VMin (After)</th>
<th>LVDI Max</th>
<th>Ploss[kw]</th>
<th>Loss Reduction</th>
</tr>
</thead>
<tbody>
<tr>
<td>No DG</td>
<td></td>
<td>202.68</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Type I DG</td>
<td>Bus 08</td>
<td>0.18</td>
<td>0.119</td>
<td>0.129976</td>
<td>111.22</td>
<td>45.124%</td>
</tr>
<tr>
<td></td>
<td>Size 50%</td>
<td>0.91309</td>
<td>0.91309</td>
<td>0.94425</td>
<td>121.22</td>
<td>40.21%</td>
</tr>
<tr>
<td>Type II DG</td>
<td>Bus 08</td>
<td>0.18</td>
<td>0.119</td>
<td>0.083941091</td>
<td>109.12</td>
<td>43.89%</td>
</tr>
<tr>
<td></td>
<td>Size 50%</td>
<td>0.91309</td>
<td>0.91309</td>
<td>0.92776</td>
<td>109.12</td>
<td>43.89%</td>
</tr>
<tr>
<td>Type III DG</td>
<td>Bus 08</td>
<td>0.18</td>
<td>0.119</td>
<td>0.25774334</td>
<td>73.904</td>
<td>63.536%</td>
</tr>
<tr>
<td></td>
<td>Size 50%</td>
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<td>0.91309</td>
<td>0.95505</td>
<td>73.904</td>
<td>63.536%</td>
</tr>
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</table>

### Table 2: Values of voltages and losses for optimal location

<table>
<thead>
<tr>
<th>Cases</th>
<th>At optimal location</th>
<th>Vmin Before</th>
<th>Vmin After</th>
<th>LVDI</th>
<th>Ploss[kw]</th>
<th>Loss Reduction</th>
</tr>
</thead>
<tbody>
<tr>
<td>No DG</td>
<td></td>
<td>202.68</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Type I</td>
<td>Bus 06</td>
<td>18</td>
<td>18</td>
<td>0.129976</td>
<td>111.22</td>
<td>45.124%</td>
</tr>
</tbody>
</table>
5. Conclusion

This paper presents the analytical method of optimally allocating the distributed generators using the proposed logarithmic voltage deviation (LVDI) in the IEEE 33 bus radial distribution network. This method utilizes different types of DG’s adaptively for finding the best location of the DG’s. Results show that the location and size are the important factors in reducing the losses and improving the voltage profile of the network. The comparison among different types of DG in the IEEE 33 bus system showed that DG capable of delivering both active and reactive power gives better system performance. This can be a good guiding step for the operation of type 3 DG’s.

References